



**NS9xxx M1
Block Diagram**

GPIO Usage	
0	- SPI B DOUT
1	- SPI B DIN
2	- SPARE (routed to LCD 40-Pin Header)
3	- SPARE (routed to LCD 40-Pin Header)
4	- SPARE (routed to LCD 40-Pin Header)
5	- SPARE (routed to LCD 40-Pin Header)
6	- SPI B CLK
7	- SPI B CS
8	- SERIAL A TX (boot monitor console port)
9	- SERIAL A RX (boot monitor console port)
10	- CARDBUS QuickSwitch Enable (0 = on)
11	- TOUCH IRQ
12	- CARDBUS VCC Enable (1 = on)
13	- I2C EXPANDER IRQ
14	- SPARE (unrouted)
15	- SPARE (unrouted)
16	- USB Hub Suspend Status (input to CPU)
17	- USB PWREN (1 = on)
18	- LCD PWREN
19	- LCD HSYNC
20	- LCD CLK
21	- LCD VSYNC
22	- LCD DBEN/ACBIAS
23	- LCD LEND (Unused)
24 to 41	LCD D0 to D17
42	- SPI ADD0
43	- SPI ADD1
44	- SERIAL D TXD
45	- SERIAL D RXD
46	- SERIAL D RTS
47	- SERIAL D CTS
48	- LCD VEE ENABLE (1 = on)
49	- LCD BACKLIGHT ENABLE (1 = on)

SPI ADDRESS DECODE MAPPING:	
00	= RESERVED
01	= ADS7846
10	= SD/MMC CARD (SPI MODE)
11	= SERIAL EEPROM

I2C EXPANDER USAGE:	
0	- User Switch 0 (0 = Pressed)
1	- User Switch 1 (0 = Pressed)
2	- SD/MMC Card Detect (0 = Card Inserted)
3	- SD/MMC Write Protect (1 = Write Protected)
4	- User LED 0 (0 = On)
5	- User LED 1 (0 = On)
6	- User LED 2 (0 = On)
7	- User LED 3 (0 = On)