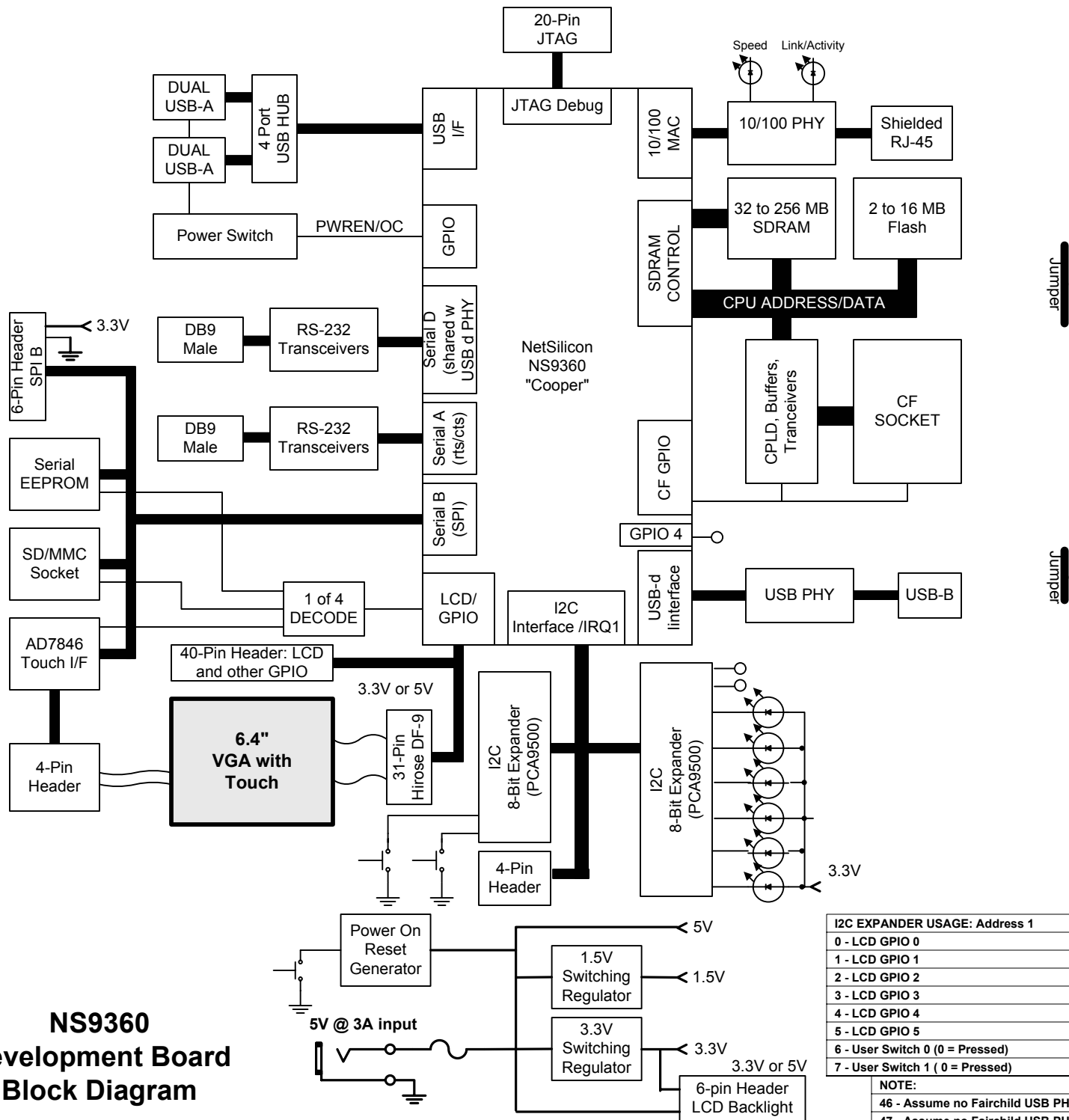


NS9360 Development Board Block Diagram



GPIO Usage	
0	- SPI B DOUT
1	- SPI B DIN
2	- SPI ADD0
3	- SPI ADD1
4	- Manuf Test LED
5	- LCD BACKLIGHT ENABLE (0 = on)
6	- SPI B CLK
7	- SPI B CS
8	- SERIAL A TXD
9	- SERIAL A RXD
10	- CF CD2 (shared w/ SERIAL A RTS)
11	- CF IRQ2 (shared w/ SERIAL A CTS)
12	- CF RESET (shared w/ SERIAL A DTR)
13	- CF CD1 (shared w/ SERIAL A DSR)
14	- CF STSCHG (shared w/ SERIAL A RI)
15	- SERIAL A DCD (shared w ext LCD clock)
16	- USB Hub in RESET (0 = hold)
17	- SPARE (Routed to Bus speed pull-down)
18	- LCD PWREN
19	- LCD HSYNC
20	- LCD CLK
21	- LCD VSYNC
22	- LCD DBEN/ACBIAS
23	- LCD VCC ENABLE (0 = on)
24 to 41	LCD D0 to D17
42	- USB VP
43	- USB VM
44	- USB OE (Shared w/ SERIAL D TXD)
45	- USB RCV (Shared w/ SERIAL D RXD)
46	- USB enum-PnP(1=on) (Shared w/ SERIAL D RTS)
47	- SERIAL D CTS)
48	- USB SPND
49	- USB SPEED
50 to 64	MII
65	- LCD Signals Enable (0=on)
66	- SD/MMC Card Detect (0 = Card Inserted)
67	- SD/MMC Write Protect (1 = Write Protected)
68	- TOUCH IRQ0
69	- I2C EXPANDERS IRQ1
70	- I2C SCL
71	- I2C SDA
72	- CF TA

} Jumper 5
} Jumper 3

SPI ADDRESS DECODE MAPPING:	
00	= RESERVED
01	= ADS7846
10	= SD/MMC CARD (SPI MODE)
11	= SERIAL EEPROM

I2C EXPANDER USAGE: Address 1	
0	- LCD GPIO 0
1	- LCD GPIO 1
2	- LCD GPIO 2
3	- LCD GPIO 3
4	- LCD GPIO 4
5	- LCD GPIO 5
6	- User Switch 0 (0 = Pressed)
7	- User Switch 1 (0 = Pressed)

I2C EXPANDER USAGE: Address 0	
0	- MFG Test out
1	- MFG Test in
3	- User LED 5 (0 = On)
4	- User LED 0 (0 = On)
5	- User LED 1 (0 = On)
6	- User LED 2 (0 = On)
7	- User LED 3 (0 = On)

NOTE:
 46 - Assume no Fairchild USB PHY
 47 - Assume no Fairchild USB PHY
REVISED
01/27/2005