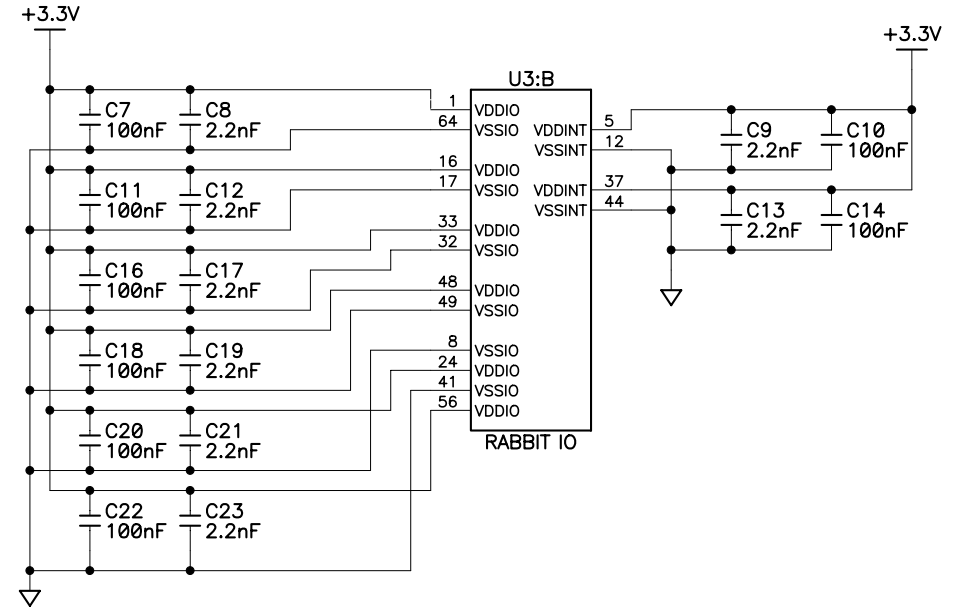
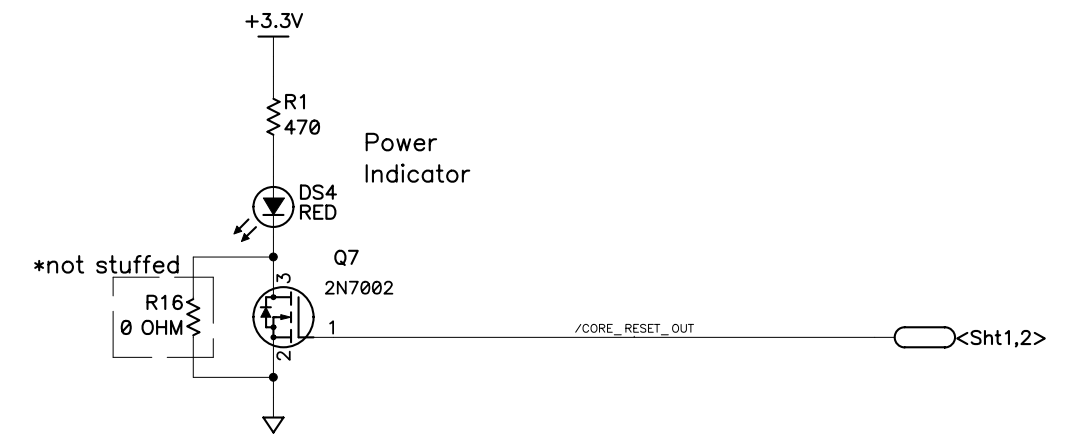


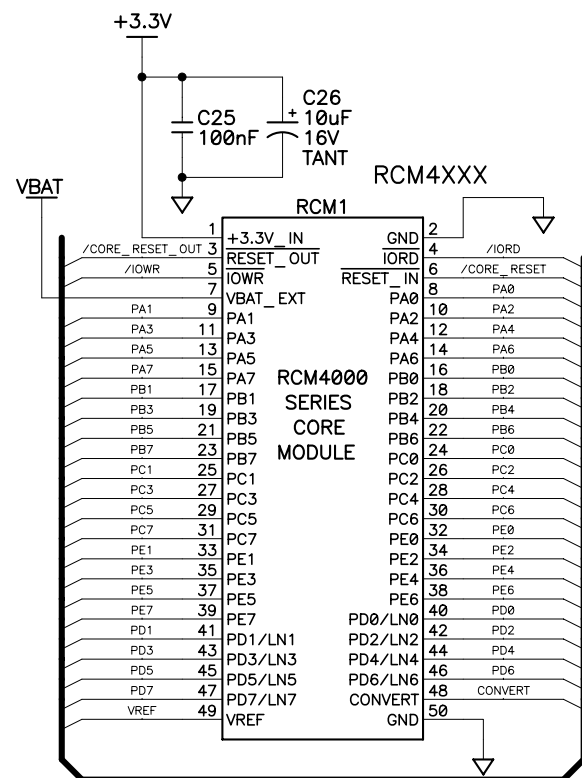
\*TABLE 1

REVISION HISTORY			REVISION APPROVAL			
REV	ECO	DESCRIPTION	PROJECT ENGINEER	APPROVAL DATE	DOCUMENT CONTROL	APPROVAL DATE
B	E14964	Converted thru-hole	HH	1/2/07	KF	1/2/07
C	E15134	Initial Release				
D	E15157	Revise Rabbit RIO Proto Board to correct pinout labeling error.				
E	E15222	Updated default jumper settings tables and offpage connectors.				
F	000888	UPDATE RIO CHIP PINOUTS AND NET NAMES				



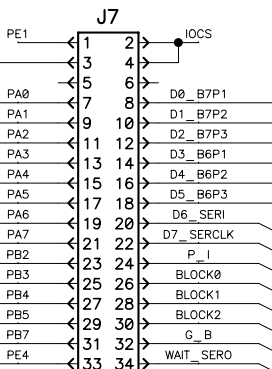
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APPEND THE FOLLOWING DOCUMENTS WHEN CHANGING THIS DOCUMENT:		DRAWING CONTENT:		TITLE		 www.rabbit.com
		DRAWN BY: (INITIAL RELEASE) HOEUN HAM	12/1/06	<b>RABBIT RIO PROTOTYPING BOARD</b>		
		REVISED BY: JEFF NAUER	1/30/09			
		APPROVALS: INITIAL RELEASE		SIZE	DWG NO.	
		PROJECT ENGINEER: HOEUN HAM		<b>B</b>	090-0231	
		ENGINEERING MANAGER: XUAN TRUONG		SCALE	NONE	RELEASE DATE
		SIGNATURES	DATE			SHEET 1 OF 4



**Core to Rio Jumpers**

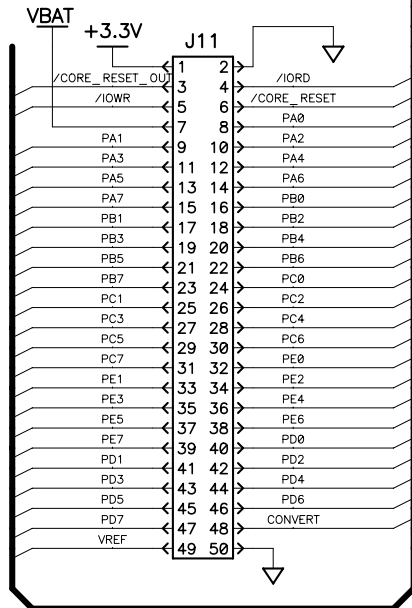
\*TABLE 2



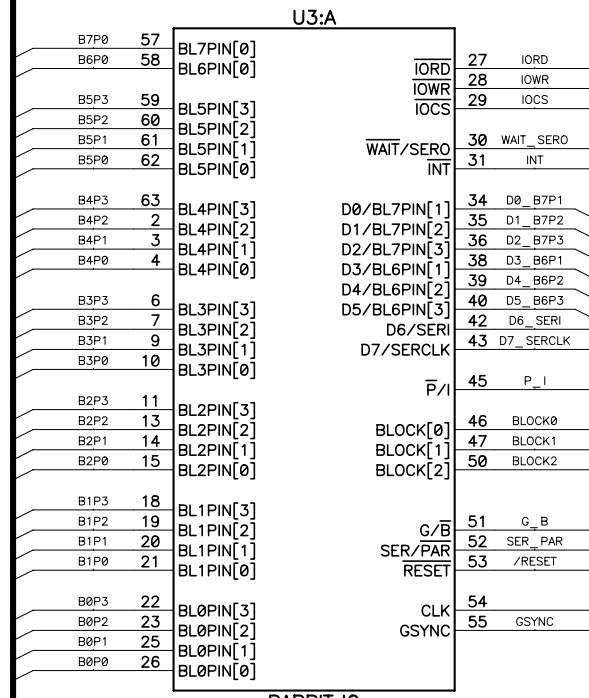
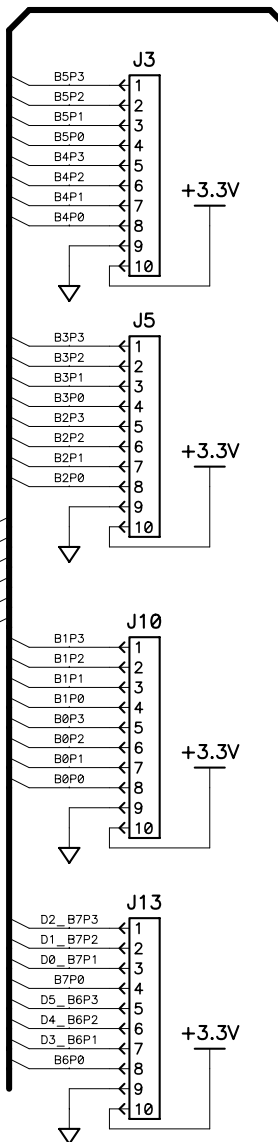
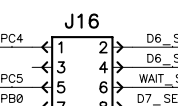
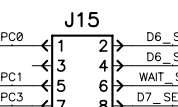
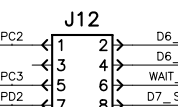
**Core Signals Bus**



**RCM4XXX Signal Header**

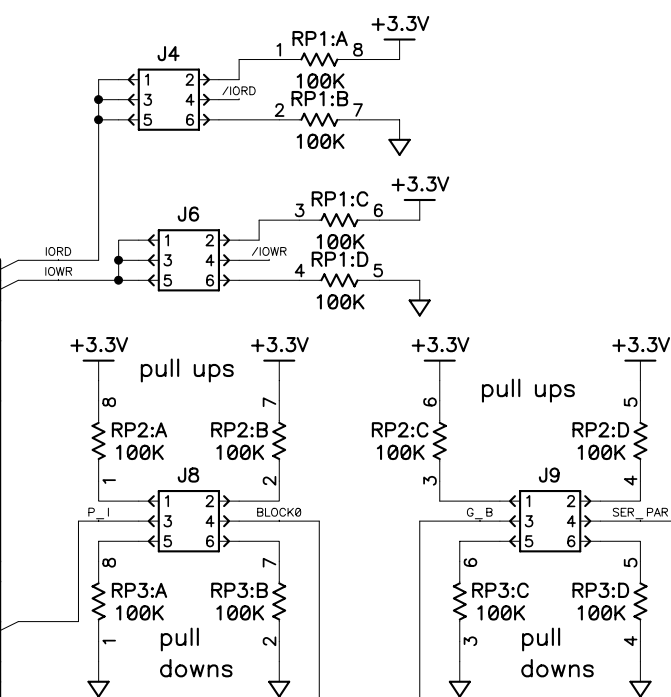


\*TABLE 3

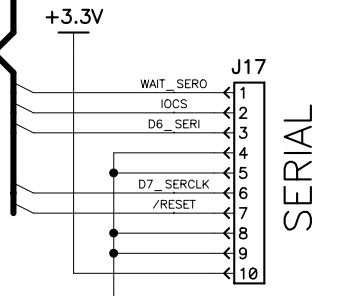
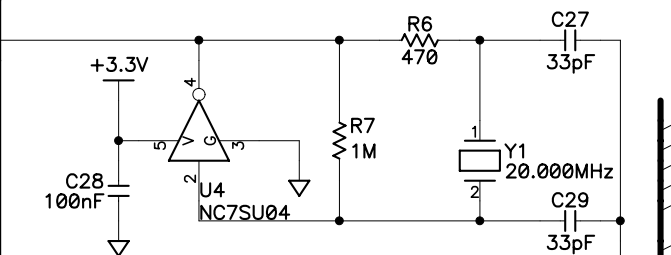
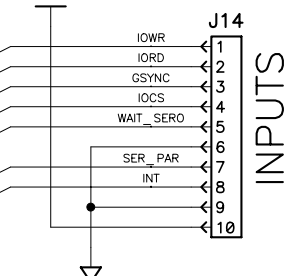


**Channel Signals Bus**  
**Control Bus**

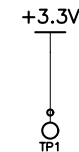
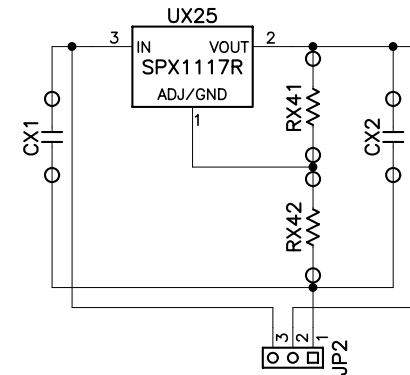
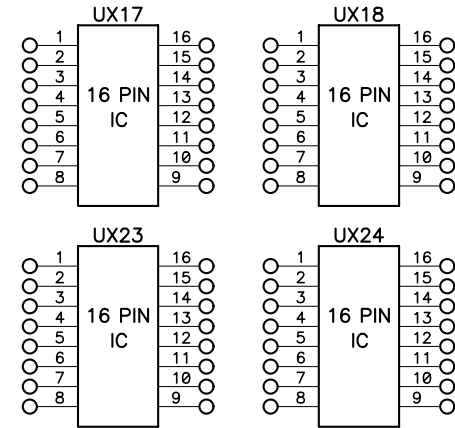
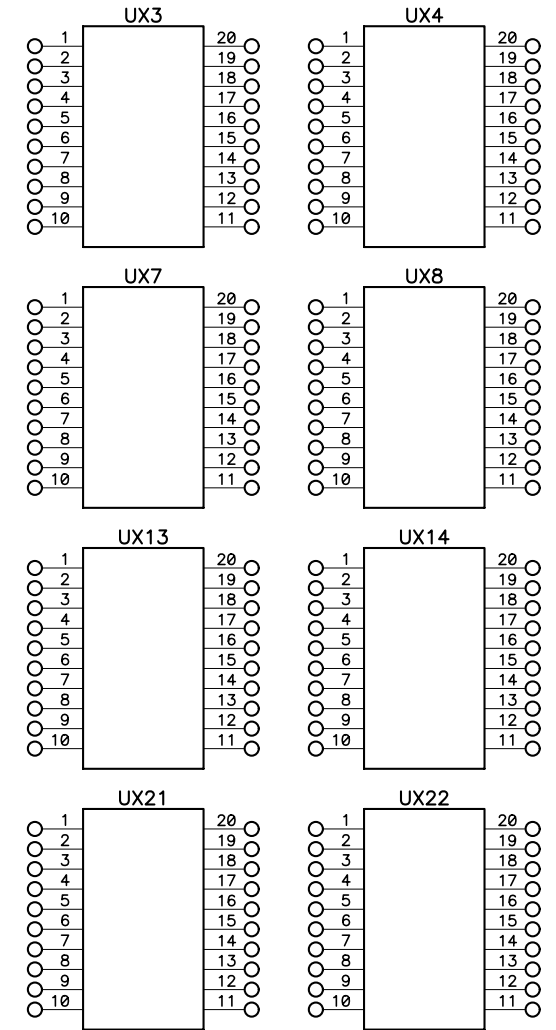
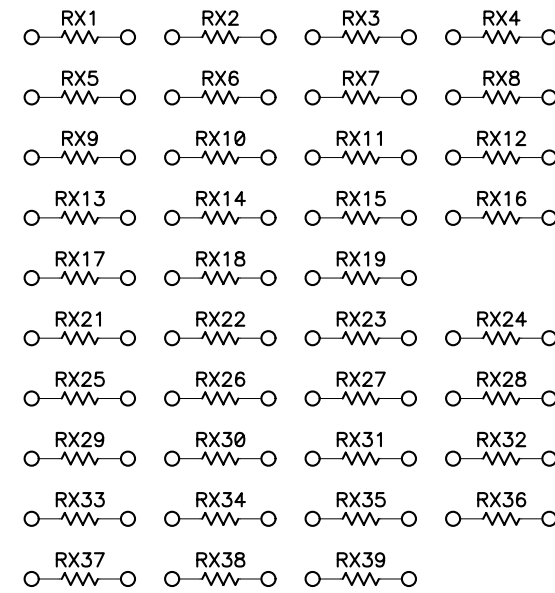
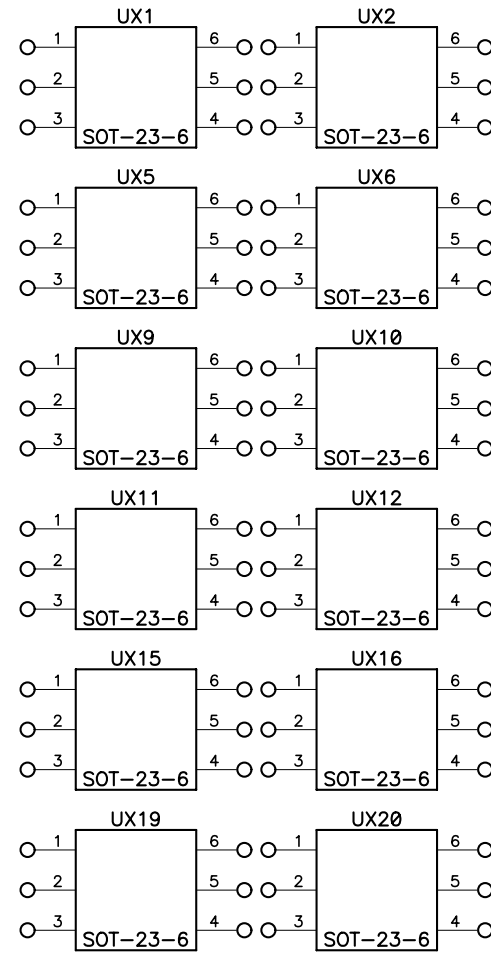
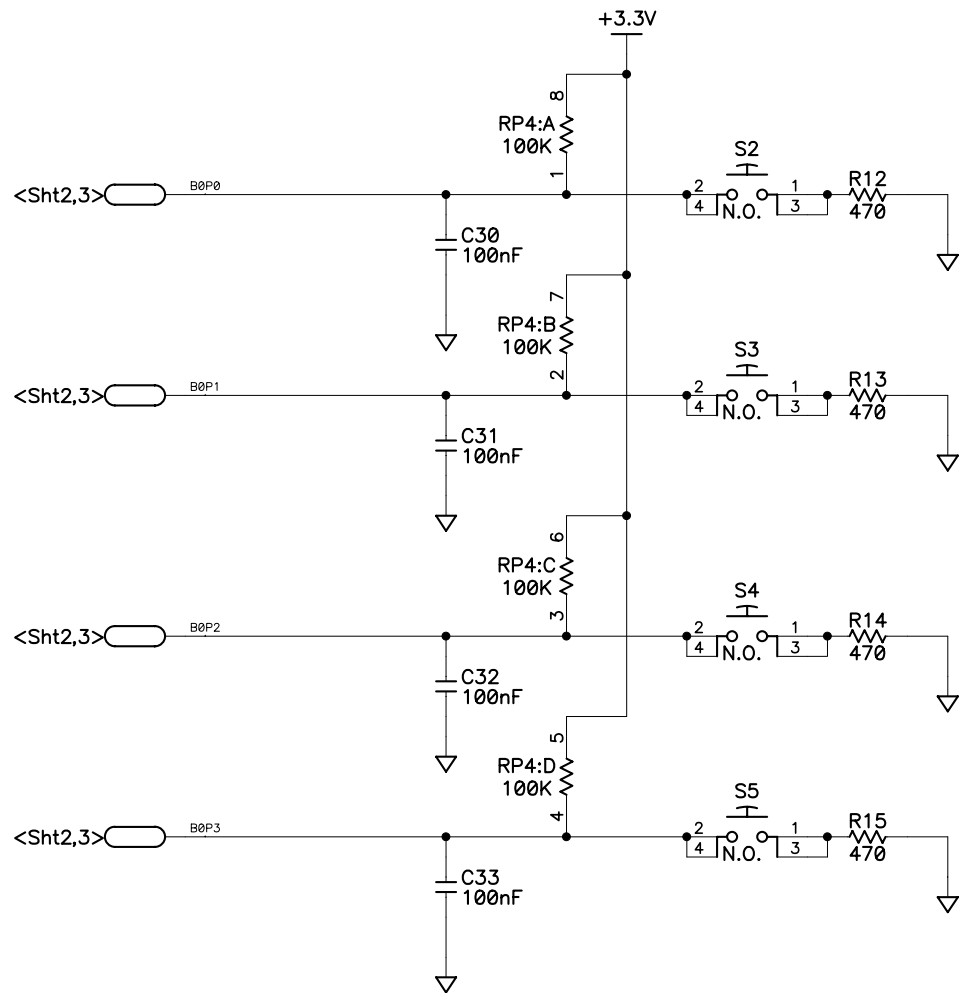
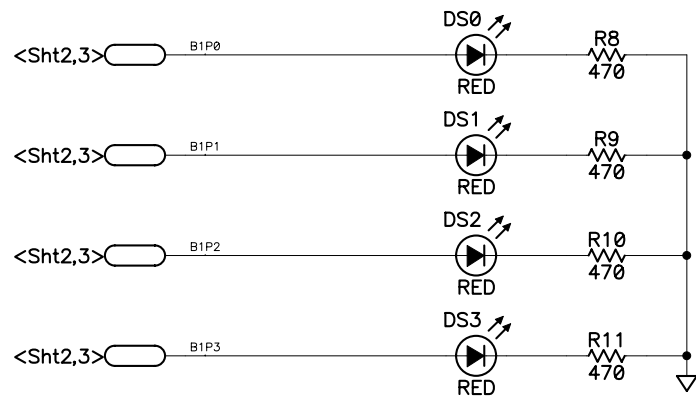
\*TABLE 4



\*TABLE 5



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\*not stuffed

\*TABLE 1: RESET JUMPERS

J2	FUNCTION
1-2	CORE RESET SAME AS RIO RESET
3-4	NOT JUMPED
5-6	RIO RESET COMES FROM PE7 (don't use S1)

] DEFAULT

\*TABLE 2: CORE MODULE  
PARALLEL MODE JUMPERS

J7	PIN FUNCTION
1-2	RIO /CS on PE1
3-4	RIO /CS PULLED LOW
5-6	nc
7-8	RIO D0 on PA0
9-10	RIO D1 on PA1
11-12	RIO D2 on PA2
13-14	RIO D3 on PA3
15-16	RIO D4 on PA4
17-18	RIO D5 on PA5
19-20	RIO D6 on PA6
21-22	RIO D7 on PA7
23-24	RIO /P/I on PB2
25-26	RIO BLOCK0 on PB3
27-28	RIO BLOCK1 on PB4
29-30	RIO BLOCK2 on PB5
31-32	RIO G//B on PB7
33-34	RIO /WAIT on PE4

] DEFAULT

\*TABLE 3: CORE MODULE  
SERIAL MODE JUMPERS

JUMPER	PIN FUNCTION
J12:1-2	RIO SERI on PC2
J12:5-6	RIO SERO on PC3
J12:4-6	BIDIRECTIONAL DATA on PC2
J12:7-8	RIO SERCLK on PD2
CORE SERIAL PORT C	
J15:1-2	RIO SERI on PC0
J15:5-6	RIO SERO on PC1
J15:4-6	BIDIRECTIONAL DATA on PC0
J15:7-8	RIO SERCLK on PC3
CORE SERIAL PORT D	
J16:1-2	RIO SERI on PC4
J16:5-6	RIO SERO on PC5
J16:4-6	BIDIRECTIONAL DATA on PC4
J16:7-8	RIO SERCLK on PB0
CORE SERIAL PORT B	

] DEFAULT  
] DEFAULT

\*TABLE 4 READ/WRITE STROBE JUMPERS

J4	FUNCTION
1-2	RIO /RD PULLED HIGH
3-4	RIO /RD IS CORE /IORD
5-6	RIO /RD PULLED LOW
J6	
1-2	RIO /WR PULLED HIGH
3-4	RIO /WR IS CORE /IOWR
5-6	RIO /WR PULLED LOW

\*TABLE 5 MODE SELECT JUMPERS

JUMPER SETTINGS				MODE	LOGIC VALUE			
$\bar{P}$ I	BLOCK0	$\frac{G}{B}$	SER PAR		$\bar{P}$ I	BLOCK0	$\frac{G}{B}$	SER PAR
X	X	X	J9:4-6	Parallel	X	X	X	0
J8:3-5	X	J9:3-5	J9:2-4	RabbitNet Device	0	X	0	1
J8:1-3	X	J9:3-5	J9:2-4	RabbitNet Hub	1	X	0	1
J8:3-5	J8:4-6	J9:1-3	J9:2-4	SPI - LSB first	0	0	1	1
J8:1-3	J8:4-6	J9:1-3	J9:2-4	Bidir Data - LSB first	1	0	1	1
J8:3-5	J8:2-4	J9:1-3	J9:2-4	SPI - MSB first	0	1	1	1
J8:1-3	J8:2-4	J9:1-3	J9:2-4	Bidir Data - MSB first	1	1	1	1

] DEFAULT