									90000626_A															
GPIO pin	**Function 00**			**Function 01**	**Function 02**																			
(Function 03	**Uart**	**HDLC**	**SPI**			**"Fixed" GPIO**	SER B	SER A	SER C	SER D	SPI B	SPI A	SPI C	SPI D	HDLC B	HDLC A	GPIO	Timers	INT	LCD	1284	DMA	PCI	USB
0	Ser B TxData	Ser B TxData	SPI B Dout	DMA Chan 0 Done dupe	Timer 1 Dupe		R486				RN42				RN44		R370	R190 (Timer1d)				R425(DONE0d)		
1	Ser B RxData	Ser B RxData	SPI B Din	DMA Chan 0 Req dupe	Ext IRQ 0		R151				RN42				RN44		R371		R496(INT0)			R423(REQ0d)		
2	Ser B RTS	etc., etc.	--	Timer 0	DMA Chan 1 Ack		R191								RN44		R372	R488 (Timer0)				R426 (ACK1d)		
3	Ser B CTS		--	1284 peripheral ack	DMA chan 0 req		R449								RN44		R373				RN35 nACK	R427 (REQ0)		
4	Ser B DTR		--	1284 peripheral busy	DMA chan 0 done	PD3 (SRAM)	R180								RN44		R374				RN35 BUSY	R429 (DONE0)		
5	Ser B DSR		--	1284 peripheral error	Dma chan 0 ack	PD2 (SRAM)	R150								RN44		R375				RN35 PERROR	R428 (ACK0)		
6	Ser B Ring Ind		SPI B Clk	1284 paper jam	Timer 7 dupe	PD1 (SRAM)	R422				RN42				RN44		R376	R509 (Timer7d)			RN35 PAPER JAM			
7	Ser B DCD		SPI B enable	DMA Chan 0 ack dupe	Ext IRQ 1	PD0 (SRAM)	R450				RN42				RN44		R377		R185(INT1)			R424 (ACK0d)		
8	Serial A TxData		SPI A Dout	Reserved	Reserved			R503				RN41				RN43	R378							
9	Serial A RxData		SPI A Din	Reserved	Timer 8 dupe			R146				RN41				RN43	R379	R492 (Timer8d)						
10	Serial A RTS		--	Reserved	Reserved			R192								RN43	R380							
11	Serial A CTS		--	IRQ2 dupe	Timer 0 dupe			R369								RN43	R381	R175 (Timer0d)	R497 (INT2d)					
12	Serial A DTR		--	Reserved	Reserved			R485								RN43	R382							
13	Serial A DSR		--	Ext IRQ 0 dupe	Timer 10 dupe			R145								RN43	R383	R493 (Timer10d)	R170 (INT0d)					
14	Serial A Ring Ind		SPI A Clk	Timer 1	Reserved			R138				RN41				RN43	R384	R506 (Timer1)						
15	Serial A DCD		SPI A enable	Timer 2	Timer 2			R421				RN41				RN43	R385	R489 (Timer2)						
16	USB overcurrent		--	1284 paper jam dupe	Timer 11 dupe	LED R144				R519							R394	R187 (Timer11d)			R544 PAPER JAM d			R136 USB_OC
17	USB Power relay		--	Reserved	Reserved	LED R143											R395							R137 (host); R139 (dev)
18	Ethernet CAM reject		--	LCD pwr enable	Ext IRQ 3 dupe												R396		R515 (INT3d)	RN33 POWER				
19	Ethernet CAM reject		--	LCD line-horz sync	DMA Chan 1 ack												R397			RN33 LP		R439 (ACK1)		
20	Ser C DTR		--	LCD Clk	Reserved				R176								R398			RN33 CP				
21	Ser C DSR		--	LCD frame pulse-vert	Reserved				R462								R399			RN33 FP				
22	Ser C Ring Ind		SPI C Clk	LCD AC bias-data enable	Reserved				R152				RN40				R400			RN33 AC				
23	Ser C DCD		SPI C enable	LCD line end	Timer 14 dupe				R463				RN40				R401	R495 (Timer14d)		RN33 LE				
24	Ser D DTR		--	LCD data 0	Reserved					R483							R430			RN33 D0				
25	Ser D DSR		--	LCD data 1	Timer 15 dupe					R155							R431	R186 (Timer15d)		RN33 D1				
26	Serial D Ring Ind		SPI D Clk	LCD data 2	Timer 3					R154				RN39			R432	R507 (Timer3)		R160 D2				
27	Serial D DCD		SPI D enable	LCD data 3	Timer 4					R164				RN39			R433	R490 (Timer4)		R159 D3				
28	Ext IRQ 1 dupe		--	LCD data 4	LCD data 8 dupe		R197										R434		R514 (INT1d)	RN46 D4,RN45 D8d	R540			
29	Timer 5		--	LCD data 5	LCD data 9 dupe		R534										R435	R508 (Timer5)		RN46 D5,RN45 D9d	R535			
30	Timer 6		--	LCD data 6	LCD data 10 dupe		R482										R436	R491 (Timer6)		RN46 D6,RN45 D10d	R539			
31	Timer 7		--	LCD data 7	LCD data 11 dupe												R437	R188 (Timer7)		RN46 D7,RN45 D11d				
32	Ext IRQ 2		--	1284 data 0	LCD data 8	PCI R336		R476									R386		R169 (INT2)	RN38 D8	RN34 D0		R336	VPP1EN0
33	Timer 8		--	1284 data 1	LCD data 9	PCI R335		R475									R387	R173 (Timer8)		RN38 D9	RN34 D1		R335	VPP1EN1
34	Timer 9		--	1284 data 2	LCD data 10	PCI R338		R162									R388	R510 (Timer9)		RN38 D10	RN34 D2		R338	VCCEN
35	Timer 10		--	1284 data 3	LCD data 11	PCI R337											R389	R172 (Timer10)		RN38 D11	RN34 D3		R337	CB_HOT
36	Reserved		--	1284 data 4	LCD data 12	PCI R356			R163								R390			RN37 D12	RN34 D4		R356	CVS1
37	Reserved		--	1284 data 5	LCD data 13	PCI R355			R178								R391			RN37 D13	RN34 D5		R355	CVS2
38	Reserved		--	1284 data 6	LCD data 14	PCI R357			R165								R392			RN37 D14	RN34 D6		R357	CLKRUN#
39	Reserved		--	1284 data 7	LCD data 15	GPIO(39)											R393			RN37 D15	RN34 D7			
40	Ser C TxData		SPI C Dout	Ext IRQ 3	LCD data 16	PCI_TEST R352			R484				RN40				R402		R184 (INT3)	R158 D16			R352	S1_PRS1
41	Ser C RxData		SPI C Din	Timer 11	LCD data 17	PCI_TEST R351			R153				RN40				R403	R511 (Timer11)		R157 D17			R351	S1_PRS2
42	Ser C RTS		--	Timer 12	LCD data 18	PCI_TEST R354			R194								R404	R494 (Timer12)		R156 D18			R354	S2_PRS1
43	Ser C CTS		--	Timer 13	LCD data 19	PCI_TEST R353			R451								R405	R512 (Timer13)		R196 D19			R353	S2_PRS2
44	Ser D TxData		SPI D Dout	1284 periph online	LCD data 20	GPIO(44)				R518				RN39			R406			RN36 D20	RN35 SELECT			
45	Ser D RxData		SPI D Din	1284 data strobe	LCD data 21	LED R142				R474				RN39			R407			RN36 D21	RN35 nSTROBE			
46	Ser D RTS		--	1284 auto linefeed	LCD data 22	LED R141				R502							R408			RN36 D22	RN35 AUTOFD			
47	Ser D CTS		--	1284 init	LCD data 23	LED R140				R464							R409			RN36 D23	RN35 nINIT			
48	Timer 14		--	1284 periph select	DMA Chan 1 Req	MFGI(flash_pg) R8				R195							R441	R171 (Timer14)			R538 nSELECTIN	R438 (REQ1)		
49	Timer 15		--	1284 periph logic	DMA Chan 1 done	MFGO(flash_pg) R9				R177							R442	R513 (Timer15)			R478 PLOGIC	R440 (DONE1)		
				DEFAULT															INT					
																			R487 -> Timer 0 to INT 0					
	This document is best utilized in tandem with the Hardware Reference Guide, Jumper and Component Guide, and Schematic																		R505 -> Timer 1 to INT 1			R255 for STRB		
	Its main purpose is an easy way of determining where else GPIO may be connected.																		R174 -> Timer 2 to INT 2					
																			R189 -> Timer 3 to INT 3					