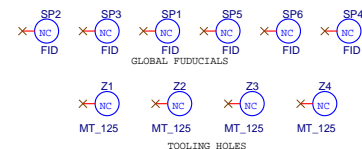


# CONFIDENTIAL MATERIAL TO NetSilicon - A Digi International Company

## NOTES:

1. ALL RESISTOR VALUES ARE IN OHMS AND IN THE 0603 SIZE UNLESS OTHERWISE NOTED
2. ALL RESISTOR VALUES ARE 5% UNLESS OTHERWISE NOTED
3. ALL CAPACITORS ARE RATED IN uFARADS AND IN THE 0603 SIZE UNLESS OTHERWISE NOTED
4. ALL CAPACITORS ARE RATED AT 50 VDC OR HIGHER UNLESS OTHERWISE NOTED
5. LAST USED:



## NS7520 DEVELOPMENT BOARD

### BOARD REVISIONS

07/15/02 1952000 REV A

### BOARD REV NOTES

### SHEET REV NOTES

March 8, 2003: Added Cover sheet and Block Diagram as page 1 & 2. Bumped up rest of sheets by two page numbers and updated all title blocks to NS7520. No schematic changes were made to Pages 3-13.

Aug. 28, 2003: Block Diagram Updated (page 1 & 2)

Sep. 23, 2004: Block Diagram, CPUA & CPUB updated: (pages 1-4)

Aug. 25, 2005: 1. Corrected population option for oscillator vs crystal. (pages 2 & 3); 2. Updated sheet page references (pages 3-13); 3. Added corrections to page 2 for Vantives 16781-16782.

### SHEET DESCRIPTION

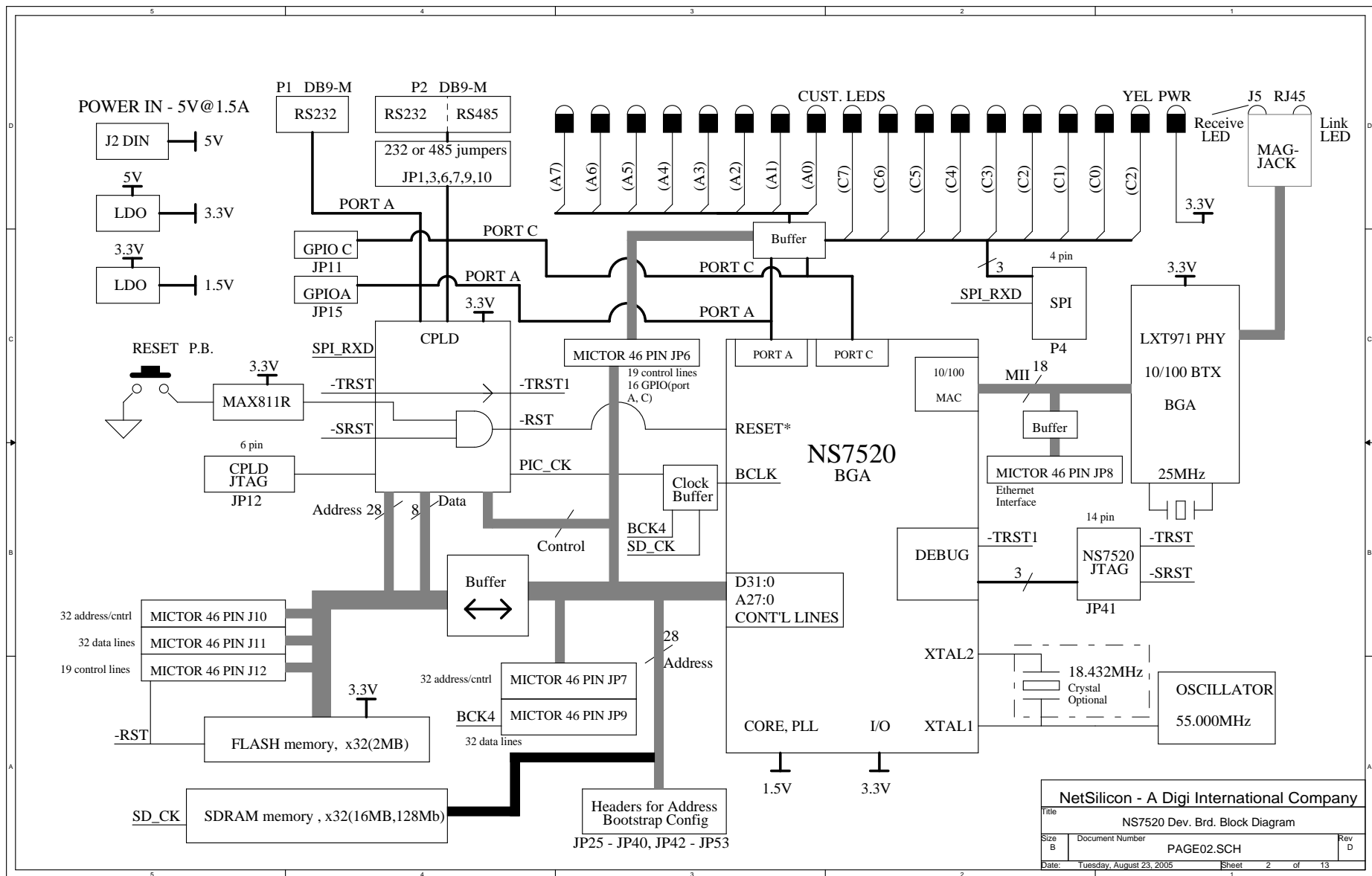
SH. #	REV.	DESCRIPTION
1.	D	Cover Sheet
2.	D	Block Diagram
3.	C	CPU-A & BCLK Buffer
4.	C	CPU-B, & CPU-C
5.	B	32/16 Bit SDRAM, Bus Transceivers
6.	B	PIC CPLD
7.	B	FLASH, SRAM, DRAM
8.	B	FPGA-A, Ext. Board Mictor Conns
9.	B	FPGA-B, -C, ISP PROM
10.	B	Serial RS232/485 Ports A & B
11.	B	Ethernet Interface
12.	B	Mictor Connectors
13.	B	Power Supply & LEDs

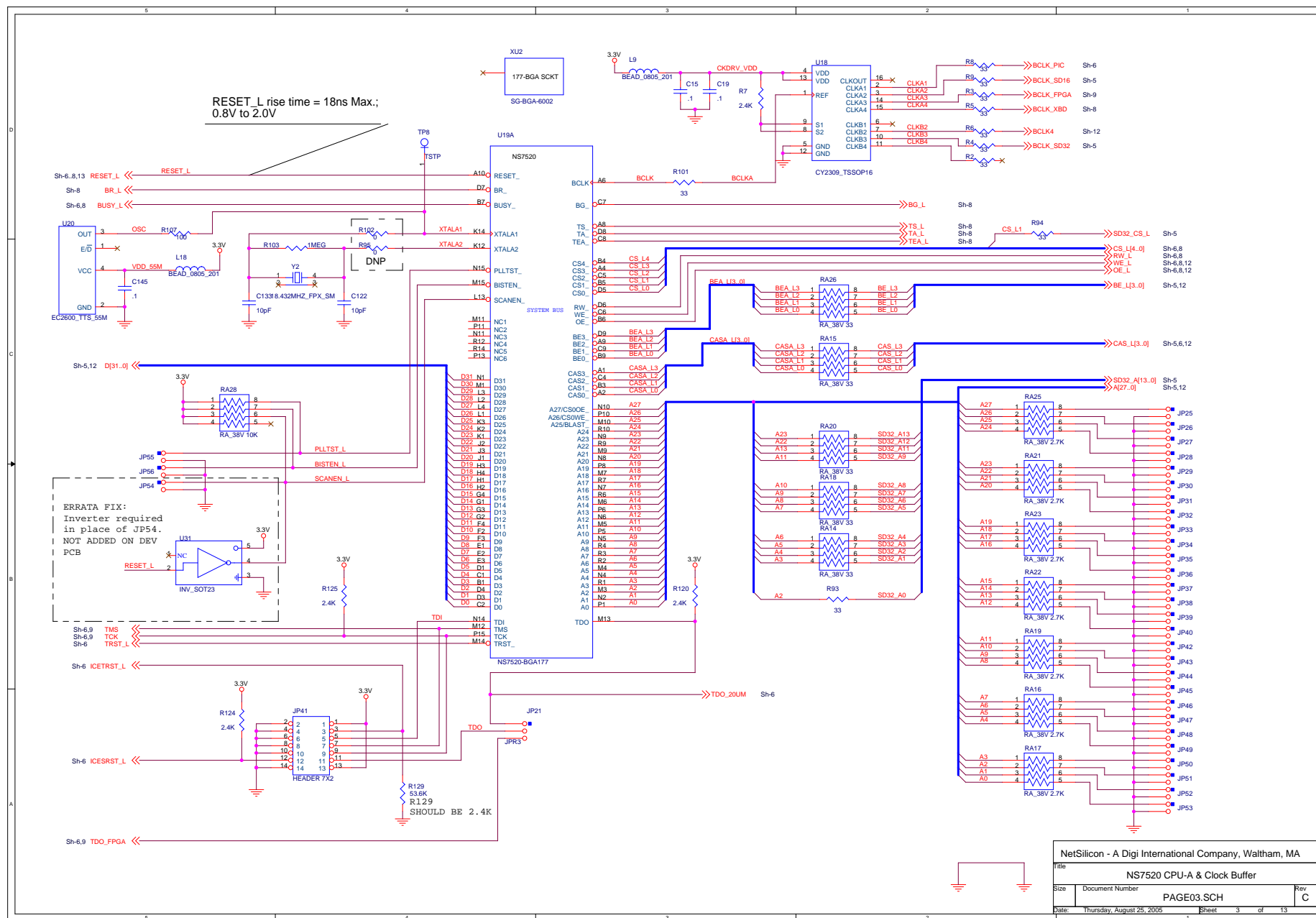
REF SCH1952000

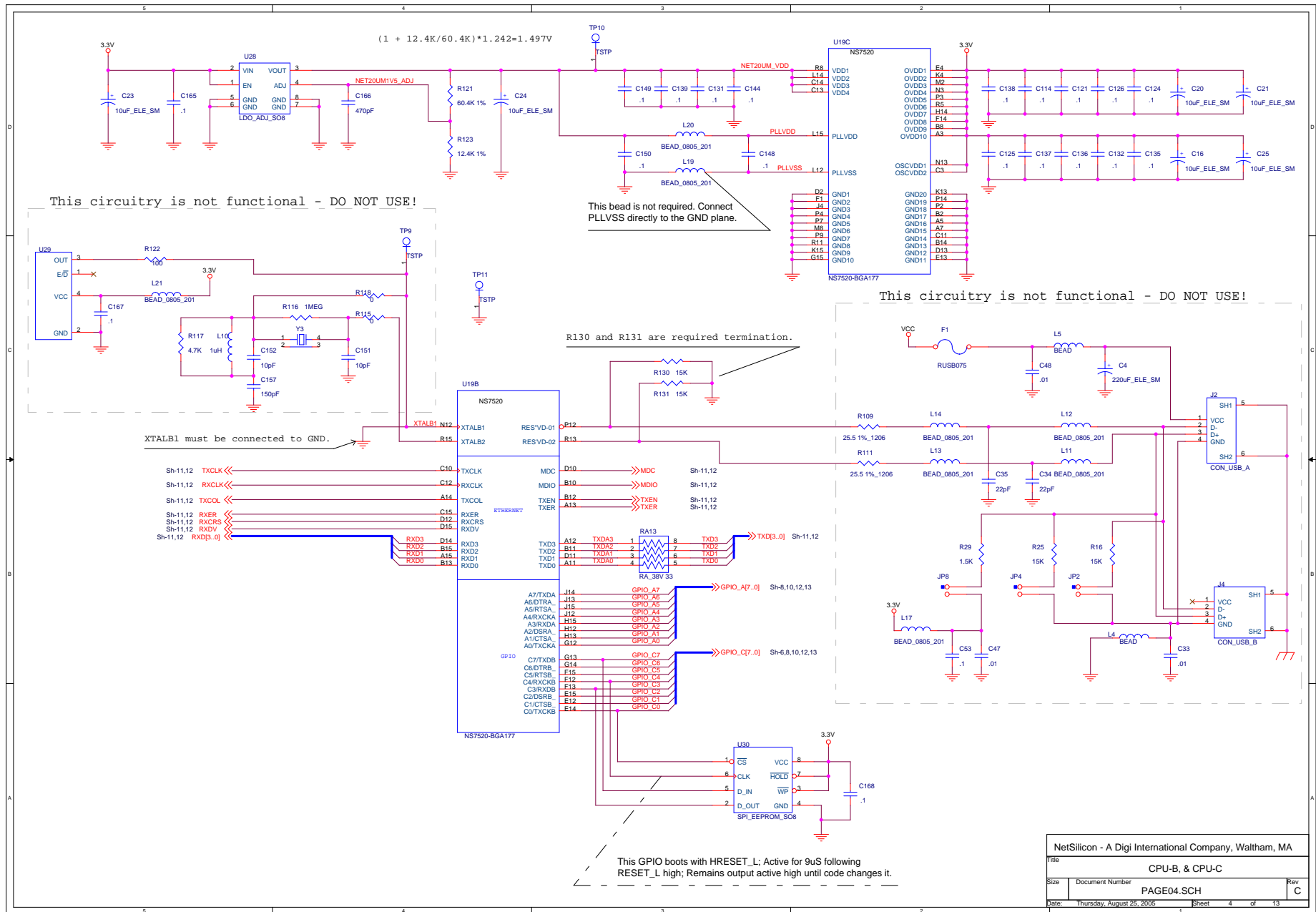
DESIGNER: Jan Szymboraki

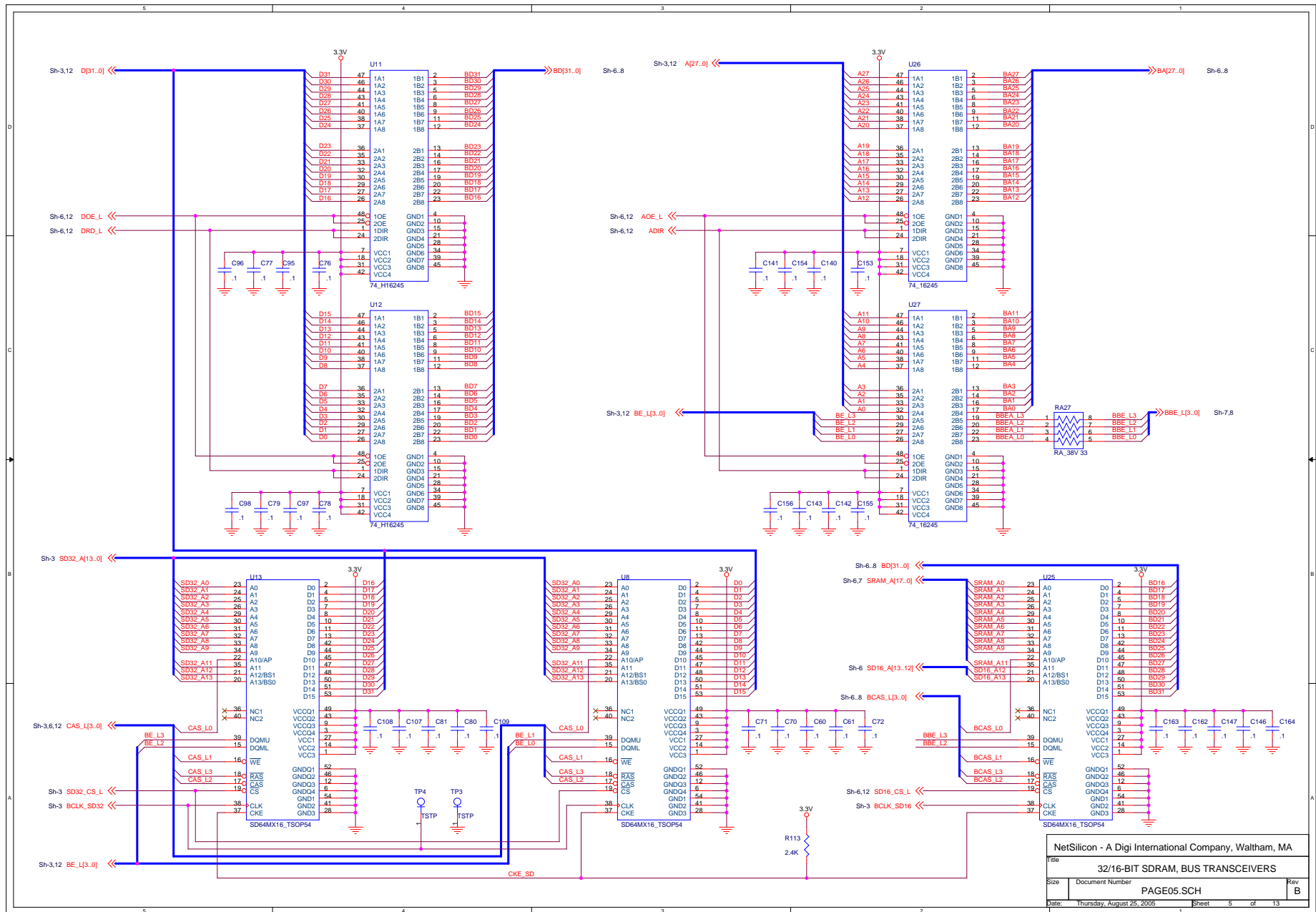
NetSilicon - A Digi International Company

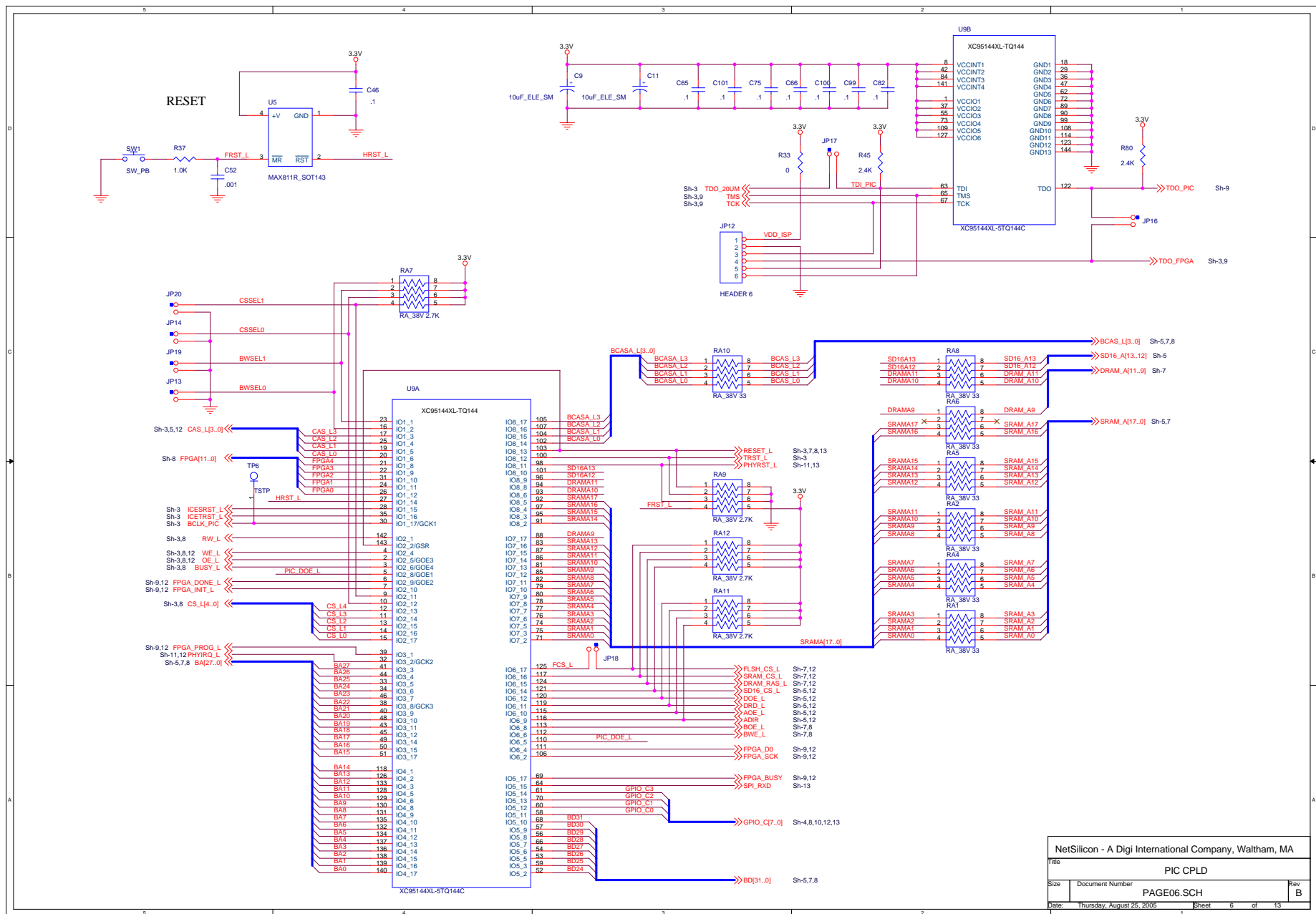
Title		
NS7520 Cover Sheet		
Size	Document Number	Rev
B	PAGE01.SCH	D
Date:	Thursday, August 25, 2005	Sheet 1 of 13

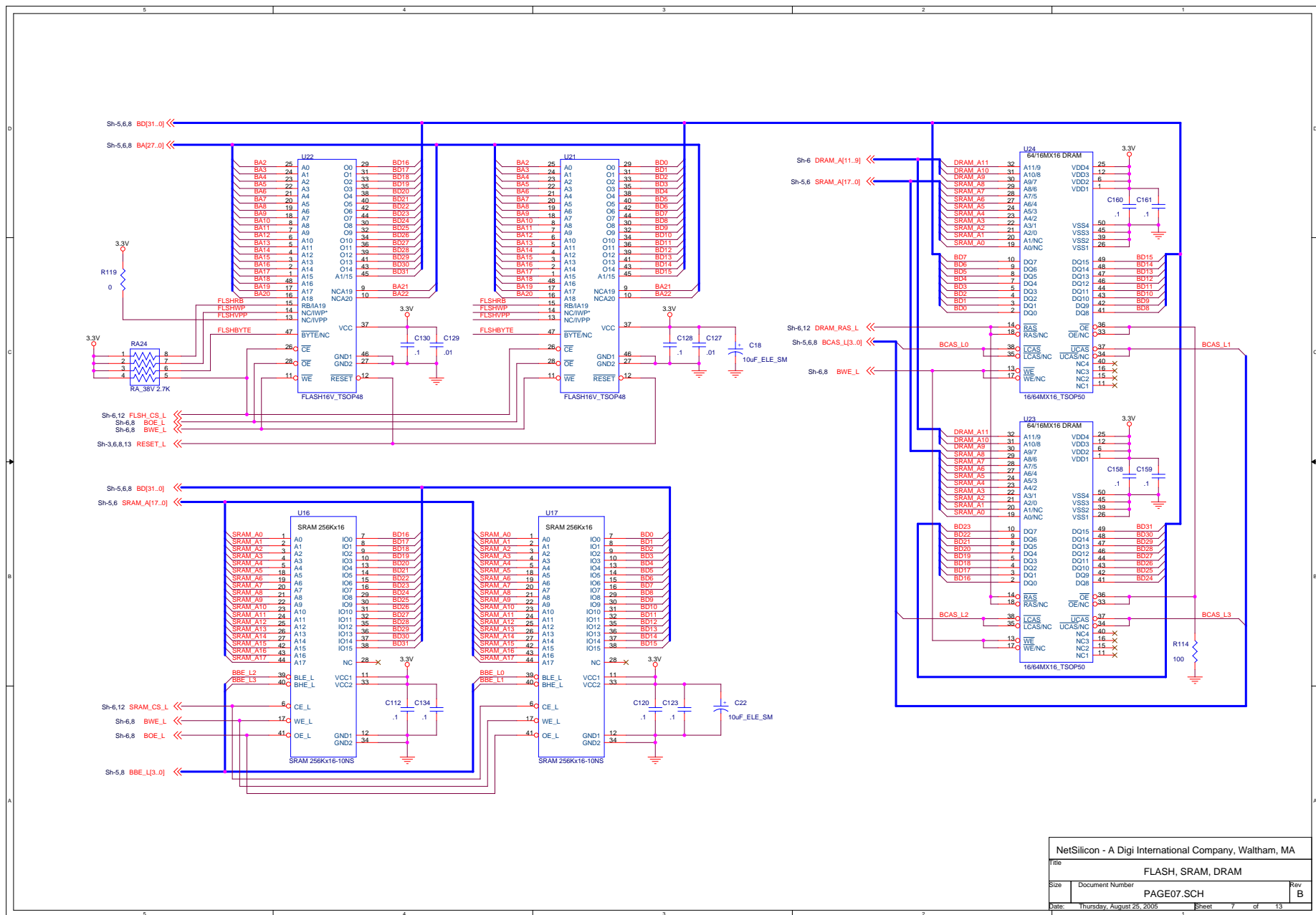


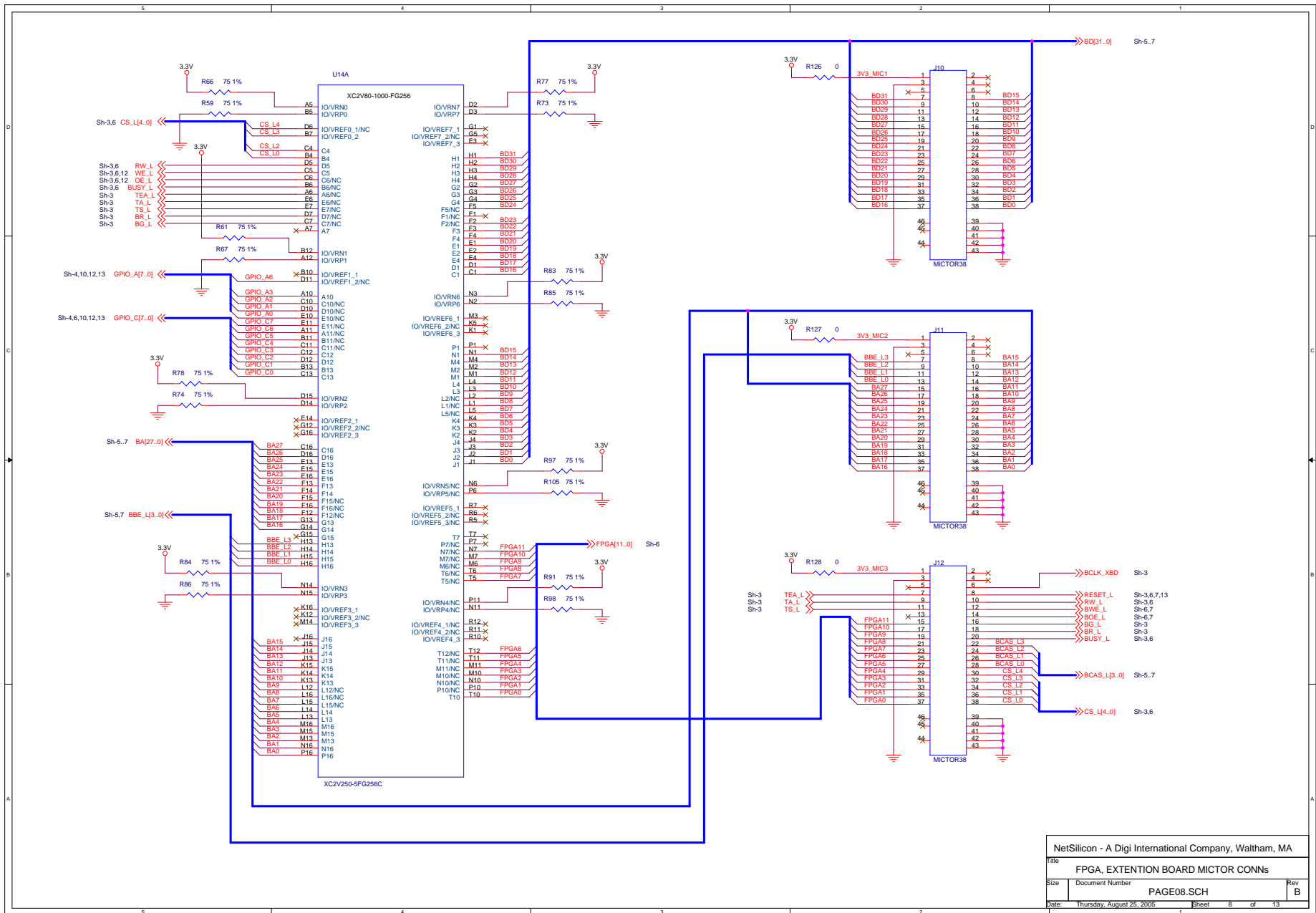




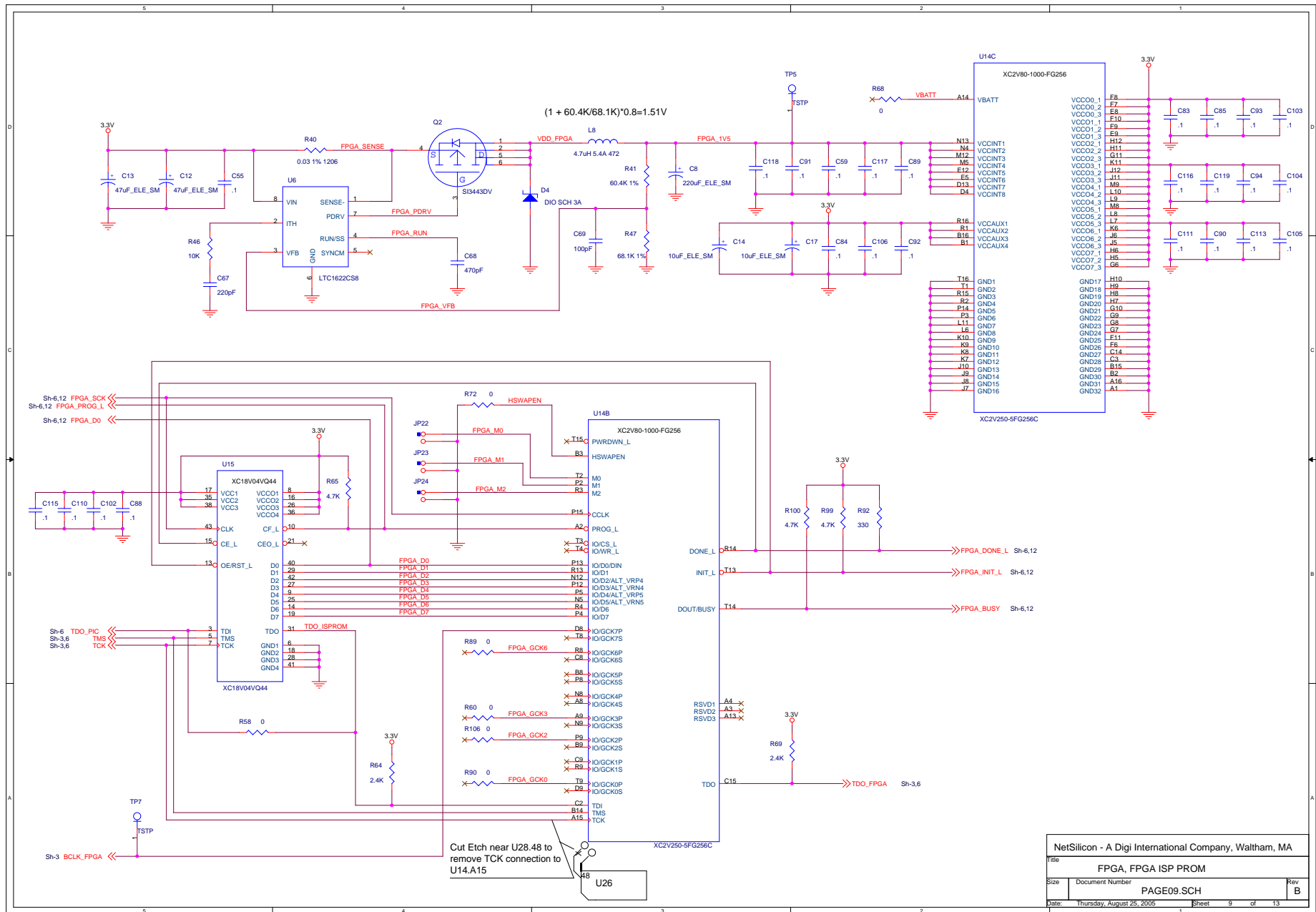




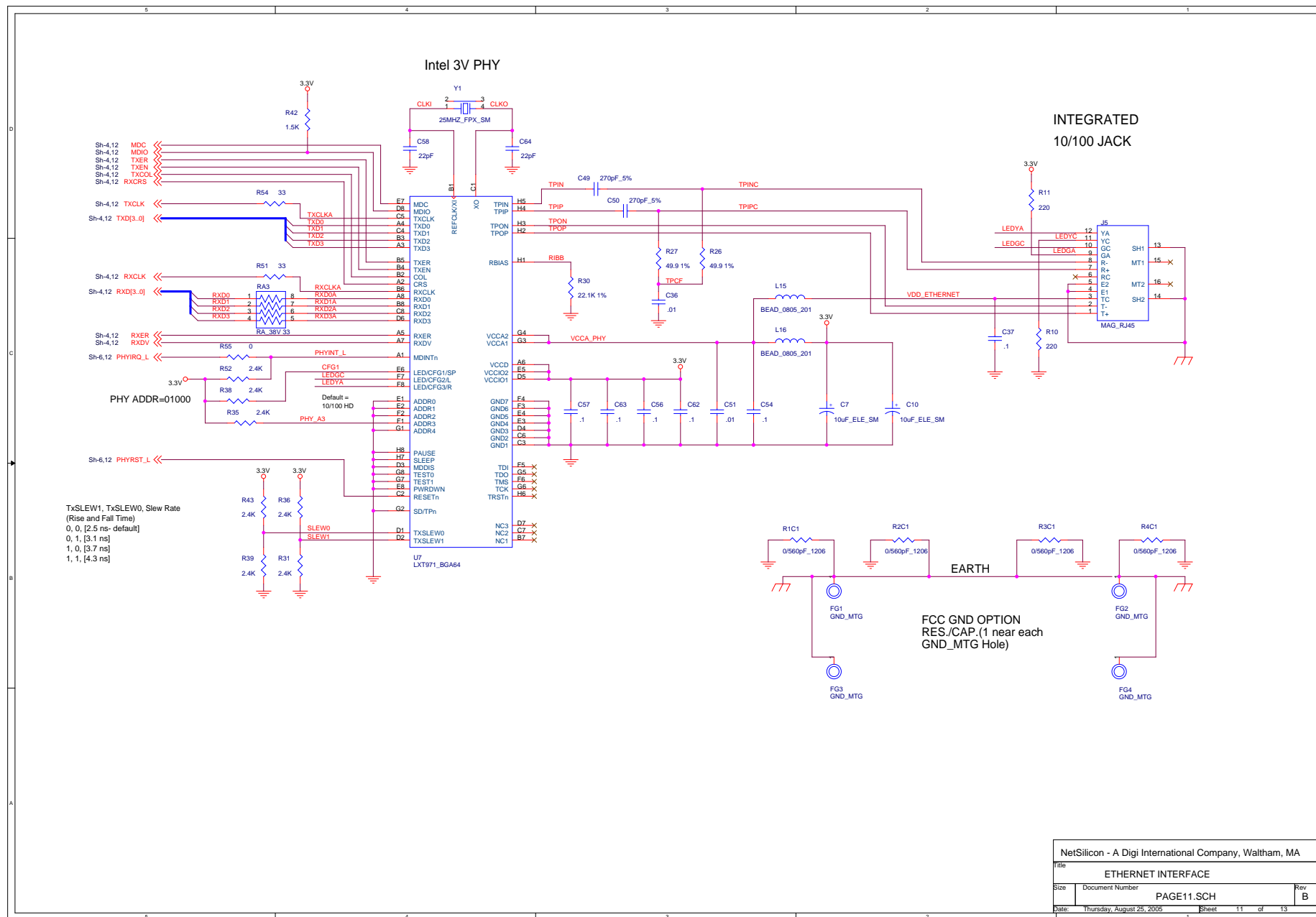


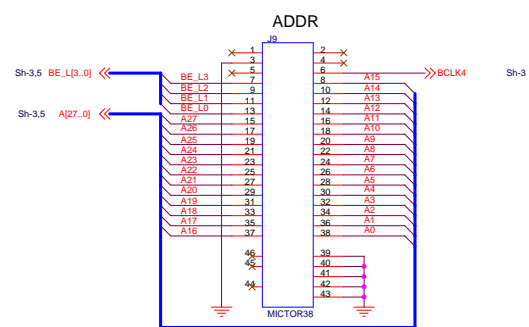
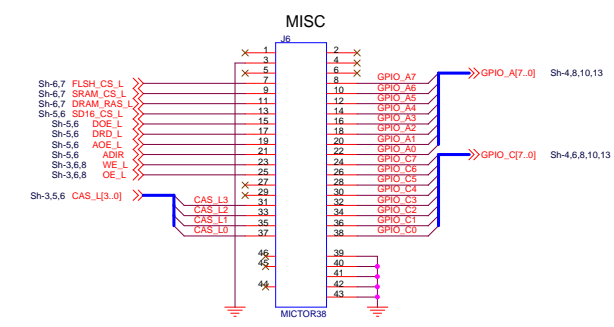
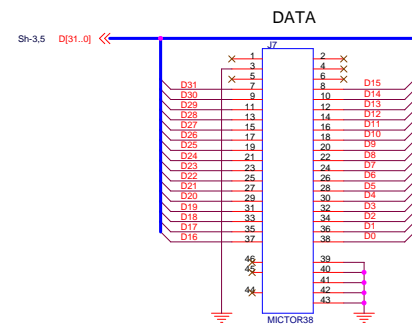
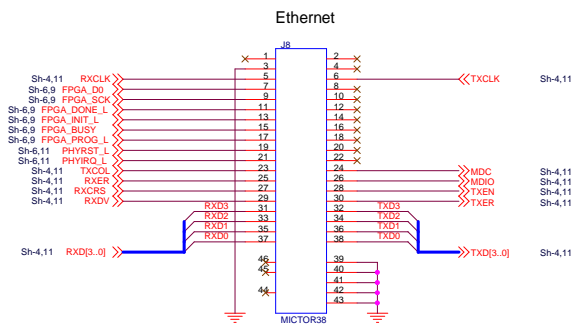












AMP #767054-1 Pin 44 = alignment hole; Pin 45-46 are for the optional latch securing holes.

