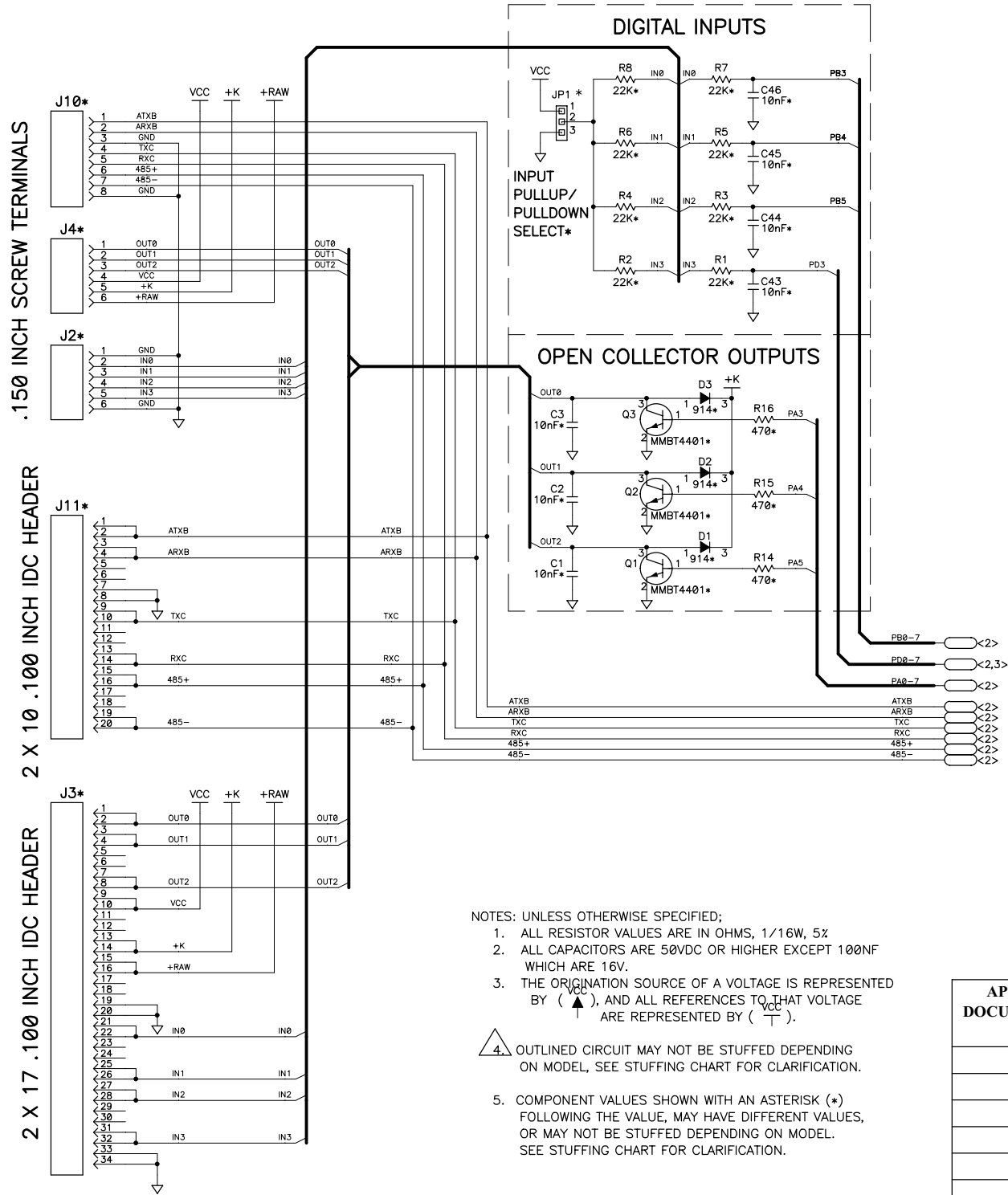


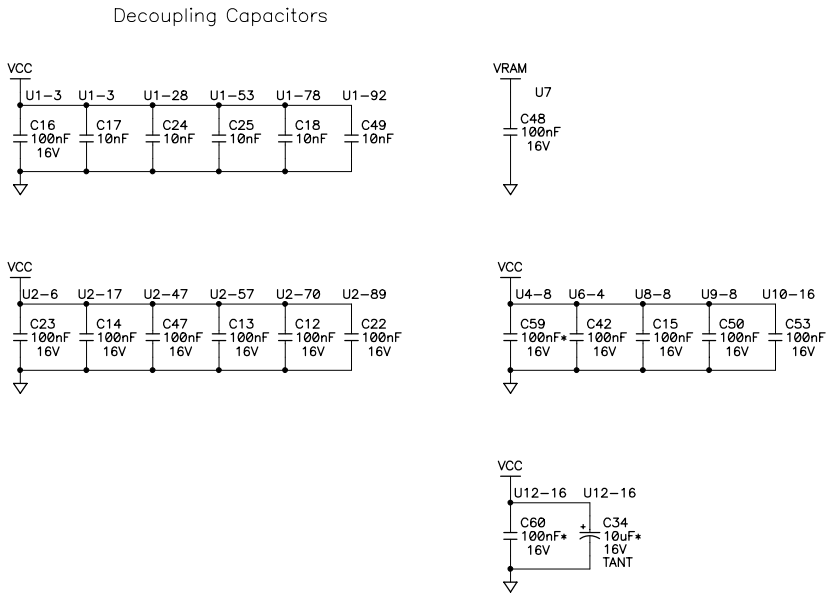
REVISION HISTORY			REVISION APPROVAL			
REV	ECO	DESCRIPTION OF CHANGE	PROJECT ENGINEER	APPROVAL DATE	DOCUMENT CONTROL	APPROVAL DATE
A	E11270	INITIAL RELEASE	RJH	2/1/01	KIS	1/25/01
B	E11697	ADDED EXTERNAL 32KHz OSCILLATOR. MODIFIED VBAT CIRCUITRY. CHANGED J5, J6 TO INTERGRATED RJ-45. ADDED RS-485 TERMINATOR JUMPER JP3.	XT	10/31/01	KIS	10/30/01
C	E11806	REMOVE EG2200, 2300 NAMES & INFO, ADD EM1500, REDUCE TO 3 PAGES.	XT	1/18/02	KIS	1/18/02



- NOTES: UNLESS OTHERWISE SPECIFIED;
- ALL RESISTOR VALUES ARE IN OHMS, 1/16W, 5%.
 - ALL CAPACITORS ARE 50VDC OR HIGHER EXCEPT 100NF WHICH ARE 16V.
 - THE ORIGIN SOURCE OF A VOLTAGE IS REPRESENTED BY (\uparrow VCC), AND ALL REFERENCES TO THAT VOLTAGE ARE REPRESENTED BY (\downarrow).
 - OUTLINED CIRCUIT MAY NOT BE STUFFED DEPENDING ON MODEL, SEE STUFFING CHART FOR CLARIFICATION.
 - COMPONENT VALUES SHOWN WITH AN ASTERISK (*) FOLLOWING THE VALUE, MAY HAVE DIFFERENT VALUES, OR MAY NOT BE STUFFED DEPENDING ON MODEL. SEE STUFFING CHART FOR CLARIFICATION.

TABLE A

REF DES	DEVICE	DEVICE VOLTAGE INFORMATION				DEVICE: FILTER CAP REF DES(s)
		GND	VCC	VRAM	NO CONNECTS	
U1	RABBIT 2000	2,27,39 52,77,89	3,28, 53, 78,92			PIN-C16 + C17, C24, C25, C18, C49
U2	RTL8019AS	14,28,44 52,83,86	6,17,47 57,70,89			C23, C14, C47, C13, C12, C22
U4	SP483E	5	8			C59
U6	ETC811L	1	4			C42
U7	SRAM 512,128K	15		32		C48
	SRAM 32K	14		28		
U8	FLASH	24	8			C15
U9	FLASH	24	8			C50
U10	74HC257	8	16			C53
U11	LM2575-5.0				2-6,9,11,13,16,19-24	
U12	232A	15	16			C60, C34



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APPEND THE FOLLOWING DOCUMENTS WHEN CHANGING THIS DOCUMENT:		DRAWING CONTENT:		TITLE	
		DRAWN BY: (INITIAL RELEASE)		SCHEMATIC DIAGRAM EG2100 & EM1500 ETHERNET PLATFORMS	
		KAH	25JUL00		
		REVISED BY:		SIZE C	
		K.SCHALLER	JAN10,02		
		APPROVALS: INITIAL RELEASE		DWG NO. 090-0112	
		PROJECT ENGINEER:			
		Richard Hess	2/1/01	SCALE NONE	
		ENGINEERING MANAGER:			
		R.Matthews	2/1/01	RELEASE DATE	
		SIGNATURES	DATE		
				SHEET 1 OF 3	



