

- NOTES: UNLESS OTHERWISE SPECIFIED;
1. ALL RESISTOR VALUES ARE IN OHMS, 1/16W, 5%
 2. ALL CAPACITORS ARE 50VDC OR HIGHER.
 3. THE ORIGIN SOURCE OF A VOLTAGE IS REPRESENTED BY (\uparrow), AND ALL REFERENCES TO THAT VOLTAGE ARE REPRESENTED BY (\uparrow).
- OUTLINED CIRCUIT MAY NOT BE STUFFED DEPENDING ON MODEL, SEE STUFFING CHART FOR CLARIFICATION.
- COMPONENT VALUES SHOWN WITH AN ASTERISK (*) FOLLOWING THE VALUE, MAY HAVE DIFFERENT VALUES, OR MAY NOT BE STUFFED DEPENDING ON MODEL. SEE STUFFING CHART FOR CLARIFICATION.

| REVISION HISTORY | | | REVISION APPROVAL | | | |
|------------------|--------|---|-------------------|---------------|------------------|---------------|
| REV | ECO | DESCRIPTION OF CHANGE | PROJECT ENGINEER | APPROVAL DATE | DOCUMENT CONTROL | APPROVAL DATE |
| A | E11313 | INITIAL RELEASE | DM | 19Feb01 | KIS | 3/24/01 |
| B | E11491 | CORRECTED FLASH 2 SELECT TO JP2 | DM | 24Apr01 | KIS | 4/25/01 |
| C | E11522 | CHANGED C8, C9 TO 10pF | DM | 14May01 | KIS | 5/14/01 |
| D | E11580 | MODIFIED VBAT CIRCUITRY, ADDED EXTERNAL 32KHz OSC | DM | 17AUG01 | | |

TABLE A


| REF DES | DEVICE | DEVICE VOLTAGE INFORMATION | | | | | | DEVICE: FILTER CAP REF DES(s) |
|---------|---------------|----------------------------|----------------------|---------------------|------|-------|--|--|
| | | AGND | GND | VCC | VRAM | uPBAT | | |
| U1 | 74VHC245 | | 10 | 20 | | | | C5 |
| U2 | ETC811L | | 2 | 4 | | | | C12 |
| U3 | RTL8019AS | | 14,28,44 52,83,86 | 6,17,47 57,70,89 | | | | C7,10,12,13,15,16 |
| U4 | RABBIT 2000 | | 2,27,39 52,77,89 | 3,28,53, 78,92 | | 42 | | C14, 57, 52, 54, 58 C25 - PIN42 (uPBAT) |
| U5 | 74VHC244 | | 10 | 20 | | | | C21 |
| U6 | 74VHC244 | | 10 | 20 | | | | C22 |
| U10 | FLASH | | 24 | 8 | | | | C50 |
| U11 | FLASH | | 24 | 8 | | | | C55 |
| U12 | SRAM 512K X 8 | | 15 | | 32 | | | C56 |
| | SRAM 128K X 8 | | 15 | | 32 | | | |
| | SRAM 32K X 8 | | 14 | | 28 | | | |

STUFFING TABLE

| CIRCUIT | PART | RCM2100 | RCM2110 | *RCM2115 | RCM2120 | RCM2130 |
|--------------------------|--|---------------|---------------|---------------|---------------|---------------|
| SRAM | U12 | 512K | 128K | 128K | 512K | 128K |
| SRAM select | JP3 | 2-3 | 1-2 | 1-2 | 2-3 | 1-2 |
| FLASH 1 | U10 | 256K | 256K | 256K | 256K | 256K |
| FLASH 1 select | JP1 | 1-2 | 1-2 | 1-2 | 1-2 | 1-2 |
| FLASH 2 | U11 | 256K | NOT INSTALLED | NOT INSTALLED | 256K | NOT INSTALLED |
| FLASH 2 select | JP2 | 1-2 | | | 1-2 | |
| ETHERNET OPTION | U3 | INSTALLED | INSTALLED | INSTALLED | NOT INSTALLED | NOT INSTALLED |
| | J3 | NOT INSTALLED | NOT INSTALLED | INSTALLED | NOT INSTALLED | NOT INSTALLED |
| | J4 | INSTALLED | INSTALLED | NOT INSTALLED | NOT INSTALLED | NOT INSTALLED |
| | R1, R2 DS1, DS2 | INSTALLED | INSTALLED | NOT INSTALLED | NOT INSTALLED | NOT INSTALLED |
| | R6, C1, C2, C3, C4 | INSTALLED | INSTALLED | NOT INSTALLED | NOT INSTALLED | NOT INSTALLED |
| | R10, R11, R19 C1, C2, C3 C4, C7, C8 C9, C11, C12 C13, C15, C16 | INSTALLED | INSTALLED | INSTALLED | NOT INSTALLED | NOT INSTALLED |
| PORTS D-E OPTION | Y1 | INSTALLED | INSTALLED | INSTALLED | NOT INSTALLED | NOT INSTALLED |
| | R21, R24, R35 R36, R37, R38 | NOT INSTALLED | NOT INSTALLED | NOT INSTALLED | INSTALLED | INSTALLED |
| INTERRUPT/JUMPER OPTIONS | R12, R18, R25 R58, R59 | NOT INSTALLED | NOT INSTALLED | NOT INSTALLED | NOT INSTALLED | NOT INSTALLED |

*Note: RCM2115 only available for custom orders

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|---|--|-----------------------------|----------|--|--|---|--|
| APPEND THE FOLLOWING DOCUMENTS WHEN CHANGING THIS DOCUMENT: | | DRAWING CONTENT: | | TITLE SCHEMATIC DIAGRAM RCM2100 SERIES | |  2900 SPAFFORD ST. DAVIS, CA 95616 530 - 757-4616 | |
| | | DRAWN BY: (INITIAL RELEASE) | | | | | |
| | | KAH | 25JUL00 | | | | |
| | | REVISED BY: | | | | | |
| | | DM | 08/17/01 | | | | |
| | | APPROVALS: INITIAL RELEASE | | | | | |
| | | PROJECT ENGINEER: | | SIZE | | DWG NO. | |
| | | | | C | | 090-0114 | |
| | | ENGINEERING MANAGER: | | | | | |
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