

REVISION HISTORY			REVISION APPROVAL			
REV	ECO	DESCRIPTION OF CHANGE	PROJECT ENGINEER	APPROVAL DATE	DOCUMENT CONTROL	APPROVAL DATE
A	E11400	INITIAL RELEASE	RJH	6/04/01	KIS	6/04/01
B	E11691	NEW BATTERY BACKUP CIRCUITRY, BATTERY, J2, AND J3				

STUFFING TABLE



	CIRCUIT	PART	RCM2300
POWER TO VRAM SWITCH	WITH BATTERY BACKUP CIRCUITRY	R33	NOT INSTALLED
CS CONTROL SWITCH	WITH BATTERY BACKUP CIRCUITRY	R27	NOT INSTALLED
FLASH	MAIN	U3	256K FLASH
	FLASH SELECT	JP1	ZERO ohm ACROSS PINS 1–2
	FLASH TYPE	JP2	ZERO ohm ACROSS PINS 1–2
LED	RED LED CIRCUIT	R29	NOT INSTALLED
		DS1	NOT INSTALLED
BATTERY	ON BOARD BATTERY	BT1	NOT INSTALLED
EXTRA CONN.	2MM THROUGH HOLE CONNECTORS	J2	NOT INSTALLED
		J3	NOT INSTALLED

TABLE A

REF DES	DEVICE	DEVICE VOLTAGE INFORMATION				DEVICE: FILTER CAP REF DES(s)
		GND	VCC	VRAM	NO CONNECTS	
U1	ETC811L					C12
U2	RABBIT 2000	2,27,39 52,77,89	3,28,53, 78,92			C8,C10,C9,22,23
U3	FLASH	24	8			C16
U5	SRAM 128K X 8	24		8		C11

- NOTES: UNLESS OTHERWISE SPECIFIED;
- ALL RESISTOR VALUES ARE IN OHMS, 1/16W, 5%
  - ALL CAPACITORS ARE 50VDC OR HIGHER.
  - THE ORIGINATION SOURCE OF A VOLTAGE IS REPRESENTED BY (  $\overset{VCC}{\uparrow}$  ), AND ALL REFERENCES TO THAT VOLTAGE ARE REPRESENTED BY (  $\frac{VCC}{\text{---}}$  ).
  - R27, R33, J2, J3, BT1, DS1, & R29 NOT NORMALLY STUFFED.
  - COMPONENT VALUES SHOWN WITH AN ASTERISK (\*) FOLLOWING THE VALUE, MAY HAVE DIFFERENT VALUES, OR MAY NOT BE STUFFED DEPENDING ON MODEL. SEE STUFFING CHART FOR CLARIFICATION.
  - JP1 AND JP2 ARE JUMPERED POSITION 1 TO POSITION 2 BY FACTORY DEFAULT.

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APPEND THE FOLLOWING DOCUMENTS WHEN CHANGING THIS DOCUMENT:		DRAWING CONTENT:		TITLE		 2900 SPAFFORD ST. DAVIS, CA 95616 530 - 757-4616	
		DRAWN BY: (INITIAL RELEASE)					
		RJH	3/22/01	SCHEMATIC DIAGRAM RCM2300			
		REVISED BY:					
		RJH	08OCT01				
		APPROVALS: INITIAL RELEASE		SIZE		DWG NO.	
		PROJECT ENGINEER:					
		ENGINEERING MANAGER:		090–0119			
		SIGNATURES	DATE	SCALE	NONE	RELEASE DATE	SHEET 1 OF 2

