

IBIS MODEL LIST

* Because the IBIS file name had the number restriction of characters (max. 8 character), we changed the file name and the model name so that it might become within 8 characters.

< TC280C (2.5V) >

2001-8-23 (7LSI)

< INPUT BUFFER >

cell name	file name
IBUF	ibuf.ibs
IBUFD	ibufd.ibs
IBUFU	ibufu.ibs
IBUFN	ibufn.ibs
IBUFND	ibufnd.ibs
IBUFNU	ibufnu.ibs
SMTC	smtc.ibs
SMTCD	smtcd.ibs
SMTCU	smtcu.ibs

< CLOCK BUFFER >

cell name	file name
DRVC4	drvc4.ibs
DRVC4D	drvc4d.ibs
DRVC4U	drvc4u.ibs
DRVC8	drvc8.ibs
DRVC8D	drvc8d.ibs
DRVC8U	drvc8u.ibs
DRVSC4	drvsc4.ibs
DRVSC4D	drvsc4d.ibs
DRVSC4U	drvsc4u.ibs
DRVSC8	drvsc8.ibs
DRVSC8D	drvsc8d.ibs
DRVSC8U	drvsc8u.ibs

< OUTPUT BUFFER >

cell name	file name
B1	b1.ibs
B2	b2.ibs
B4	b4.ibs
B4H	b4h.ibs
B4R	b4r.ibs
B8	b8.ibs
B8H	b8h.ibs
B8R	b8r.ibs

< TRI-STATE BUFFER >

cell name	file name
BT1	t1.ibs
BT2	t2.ibs
BT4	t4.ibs
BT4H	t4h.ibs
BT4R	t4r.ibs
BT8	t8.ibs
BT8H	t8h.ibs
BT8R	t8r.ibs

< BIDIRECTIONAL OUTPUT BUFFER >

cell name	file name
BD1C	d1c.ibs
BD1CD	d1cd.ibs
BD1CU	d1cu.ibs
BD1CN	d1cn.ibs
BD1CND	d1cnd.ibs
BD1CNU	d1cnu.ibs
BD1SC	d1sc.ibs
BD1SCD	d1scd.ibs
BD1SCU	d1scu.ibs
BD2C	d2c.ibs
BD2CD	d2cd.ibs
BD2CU	d2cu.ibs
BD2CN	d2cn.ibs
BD2CND	d2cnd.ibs
BD2CNU	d2cnu.ibs
BD2SC	d2sc.ibs
BD2SCD	d2scd.ibs
BD2SCU	d2scu.ibs
BD4C	d4c.ibs
BD4CD	d4cd.ibs
BD4CU	d4cu.ibs
BD4CN	d4cn.ibs
BD4CND	d4cnd.ibs
BD4CNU	d4cnu.ibs
BD4SC	d4sc.ibs
BD4SCD	d4scd.ibs
BD4SCU	d4scu.ibs
BD4HC	d4hc.ibs
BD4HCD	d4hcd.ibs
BD4HCU	d4hcu.ibs
BD4HCN	d4hcn.ibs
BD4HCND	d4hcnd.ibs
BD4HCNU	d4hcnu.ibs
BD4HSC	d4hsc.ibs
BD4HSCD	d4hscd.ibs
BD4HSCU	d4hscu.ibs
BD4RC	d4rc.ibs
BD4RCD	d4rcd.ibs
BD4RCU	d4rcu.ibs
BD4RCN	d4rcn.ibs
BD4RCND	d4rcnd.ibs
BD4RCNU	d4rcnu.ibs
BD4RSC	d4rsc.ibs
BD4RSCD	d4rscd.ibs
BD4RSCU	d4rscu.ibs

cell name	file name
BD8C	d8c.ibs
BD8CD	d8cd.ibs
BD8CU	d8cu.ibs
BD8CN	d8cn.ibs
BD8CND	d8cnd.ibs
BD8CNU	d8cnu.ibs
BD8SC	d8sc.ibs
BD8SCD	d8scd.ibs
BD8SCU	d8scu.ibs
BD8HC	d8hc.ibs
BD8HCD	d8hcd.ibs
BD8HCU	d8hcu.ibs
BD8HCN	d8hcn.ibs
BD8HCND	d8hcnd.ibs
BD8HCNU	d8hcnu.ibs
BD8HSC	d8hsc.ibs
BD8HSCD	d8hscd.ibs
BD8HSCU	d8hscu.ibs
BD8RC	d8rc.ibs
BD8RCD	d8rcd.ibs
BD8RCU	d8rcu.ibs
BD8RCN	d8rcn.ibs
BD8RCND	d8rcnd.ibs
BD8RCNU	d8rcnu.ibs
BD8RSC	d8rsc.ibs
BD8RSCD	d8rscd.ibs
BD8RSCU	d8rscu.ibs

< change rule >

TLCH -> TL
 VI -> V
 FS -> F
 BD -> D
 BT -> T
 ST -> S
 OD -> O
 16 -> F
 24 -> G
 PCI -> P
 IF -> I
 ML -> M