

## DMA INTERFACE TIMING CHARACTERISTICS

BCLK max frequency 55.296MHz

Operating conditions:

TEMPERATURE: -15.00 (min) 110.00 (max)

VOLTAGE : 1.60 (min) 1.40 (max)

OUTPUT LOAD : 25.0pf

INPUT DRIVE : CMOS BUFFER

Num	Characteristic	Min	Max	Unit
72	BCLK high to DACK* valid		14	ns
75	BCLK high to DONE* (output) valid		15	ns
70	DREQ* low to BCLK high (setup)	5		ns
71	BCLK high to DREQ* valid (hold)	0		ns
73	DONE* (input) valid BCLK high (setup)	5		ns
74	BCLK high to DONE* (input) valid (hold)	0		ns

Figure 1. DMA Interface Timing Characteristics