

JTAG – ARM ICE INTERFACE TIMING CHARACTERISTICS

Operating conditions:

TEMPERATURE: -15.00 (min) 110.00 (max)

VOLTAGE : 1.60 (min) 1.40 (max)

OUTPUT LOAD : 25.0pf

INPUT DRIVE : CMOS BUFFER

Num	Characteristic	Min	Max	Unit
54	TCK low to TDO valid		21	ns
55	TCK low to TDO HighZ		21	ns
56	TDI setup to TCK rising	1		ns
57	TDI hold from TCK rising	3		ns
58	TRST_width	1		Ttck
60	TMS setup to TCK rising	1		ns
61	TMS hold to TCK rising	3		ns

Figure 1. JTAG-ARM ICE Timing Interface