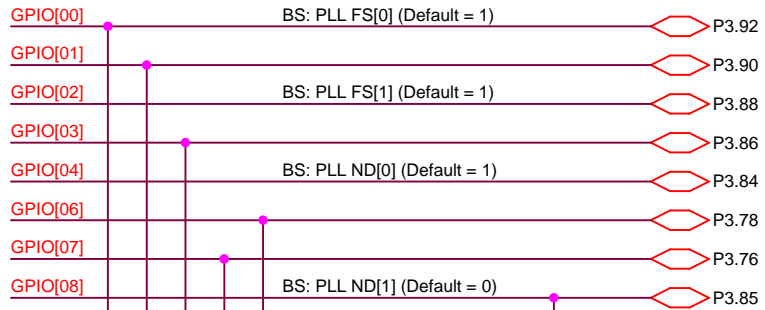
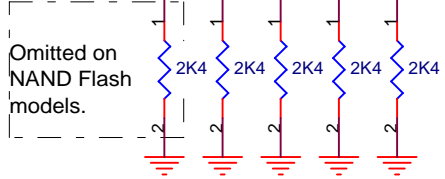
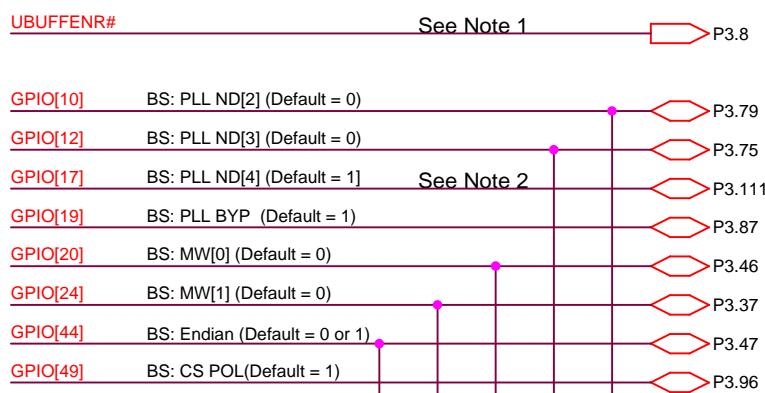


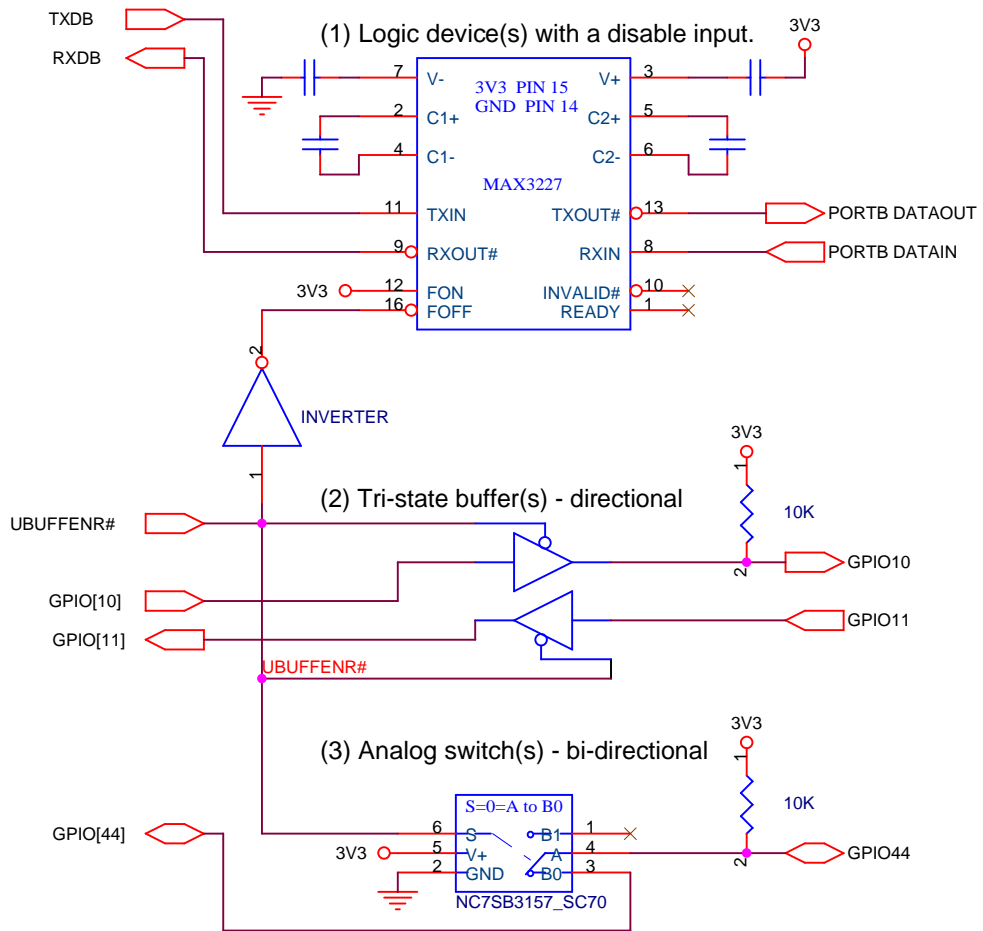
CC9C / Wi-9C Module



Used on NAND flash models. Isolated when UBUFFENR# = 0.



Baseboard - 3 Isolation Examples



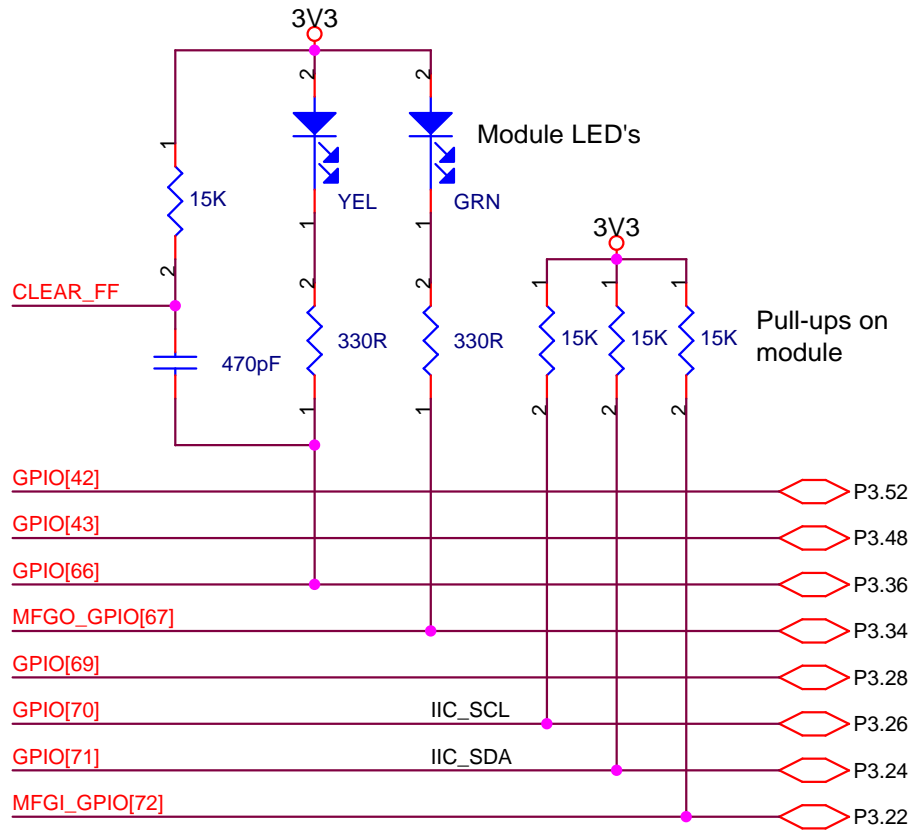
Note 1: UBUFFENR# - 24ma driver used to hold baseboard drivers off until boot is done. Inputs must not be driven until this output = "0". Isolation protects GPIO pins with bootstrap(BS) from being latched incorrectly during boot, and conversely protects baseboard inputs from being effected by bootstrap 2K4 pull-down resistors.

Note 2: GPIO[17] and GPIO[16] are normally reserved for external USB host functions. See Ext. USB Host drawing.

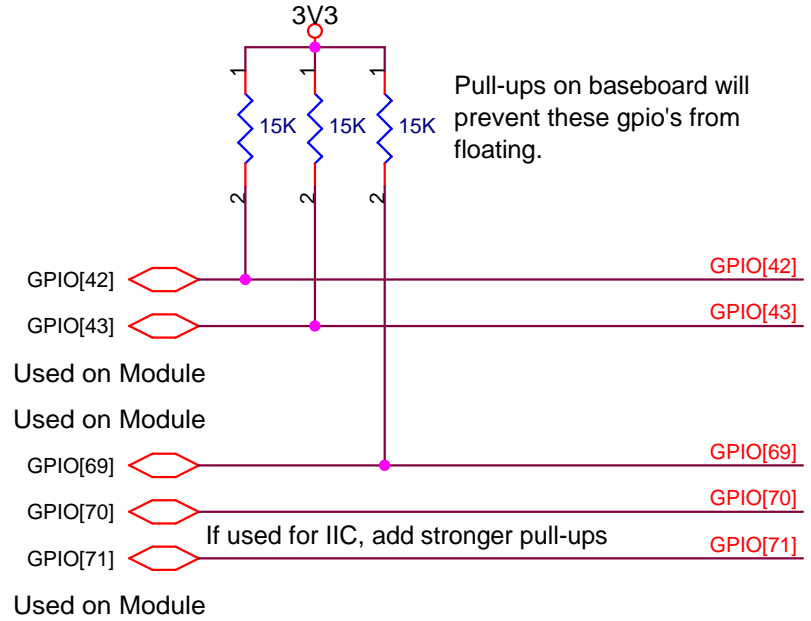
BootStrap Isolation Considerations

REV B corrects connection to GPIO[06]

CC9C / Wi-9C Module



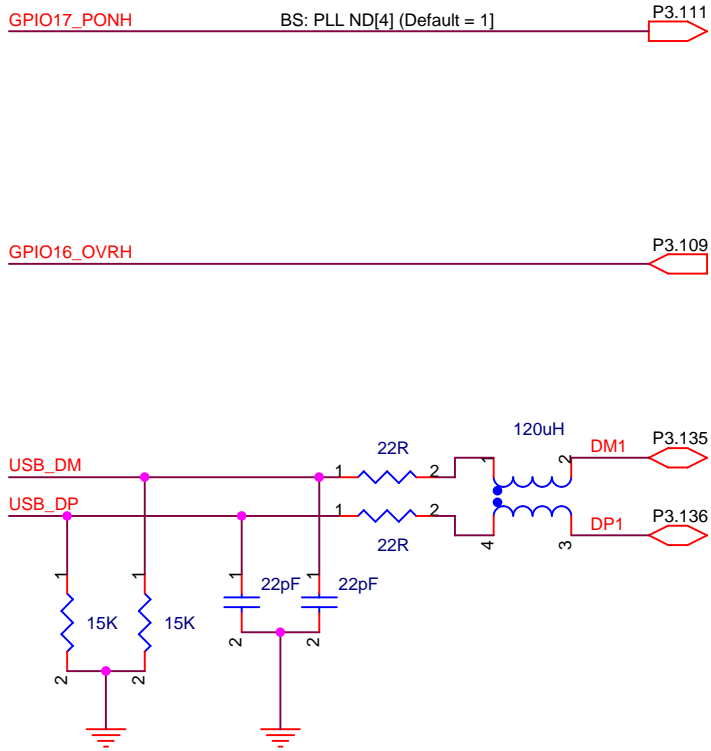
Baseboard



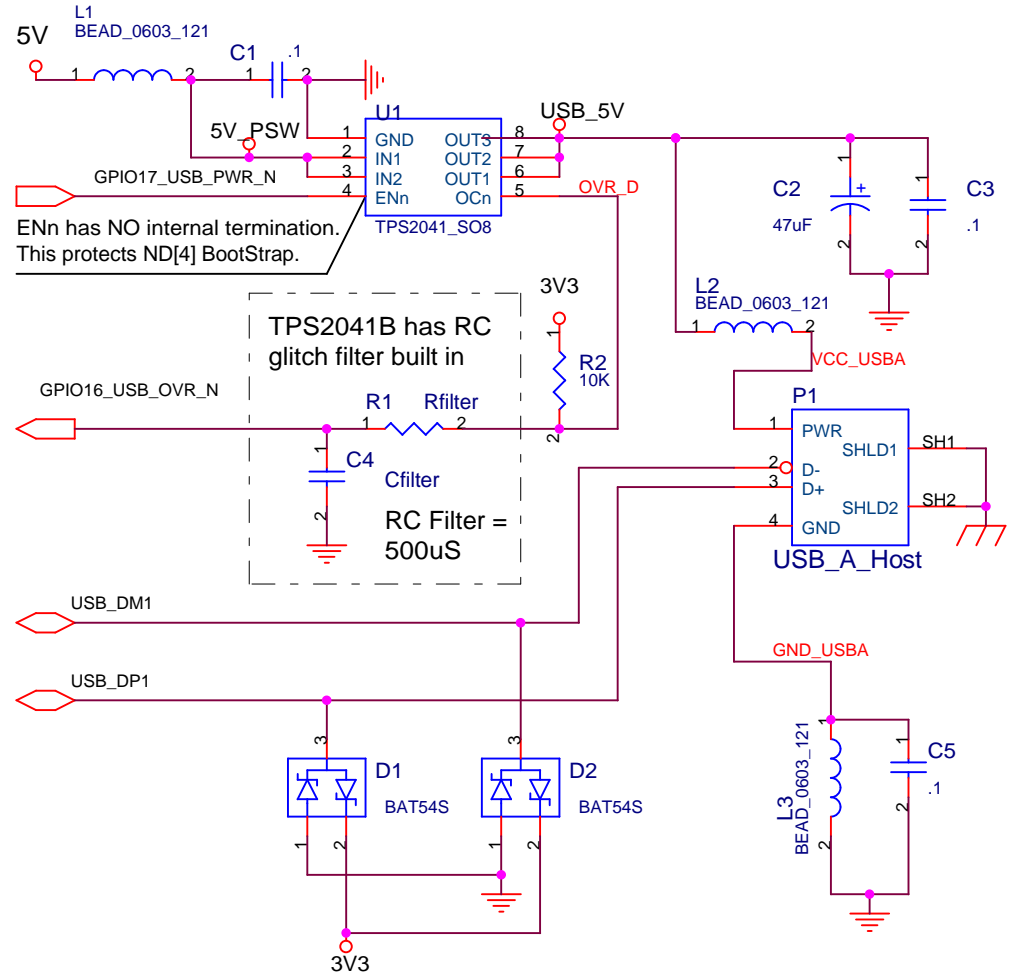
Special Attention GPIOs

CC9C / Wi-9C Module

Code initializes USB registers, then sets GPIO[16] and GPIO[17] to mode 0 - USB, with the inversion function enabled for both.

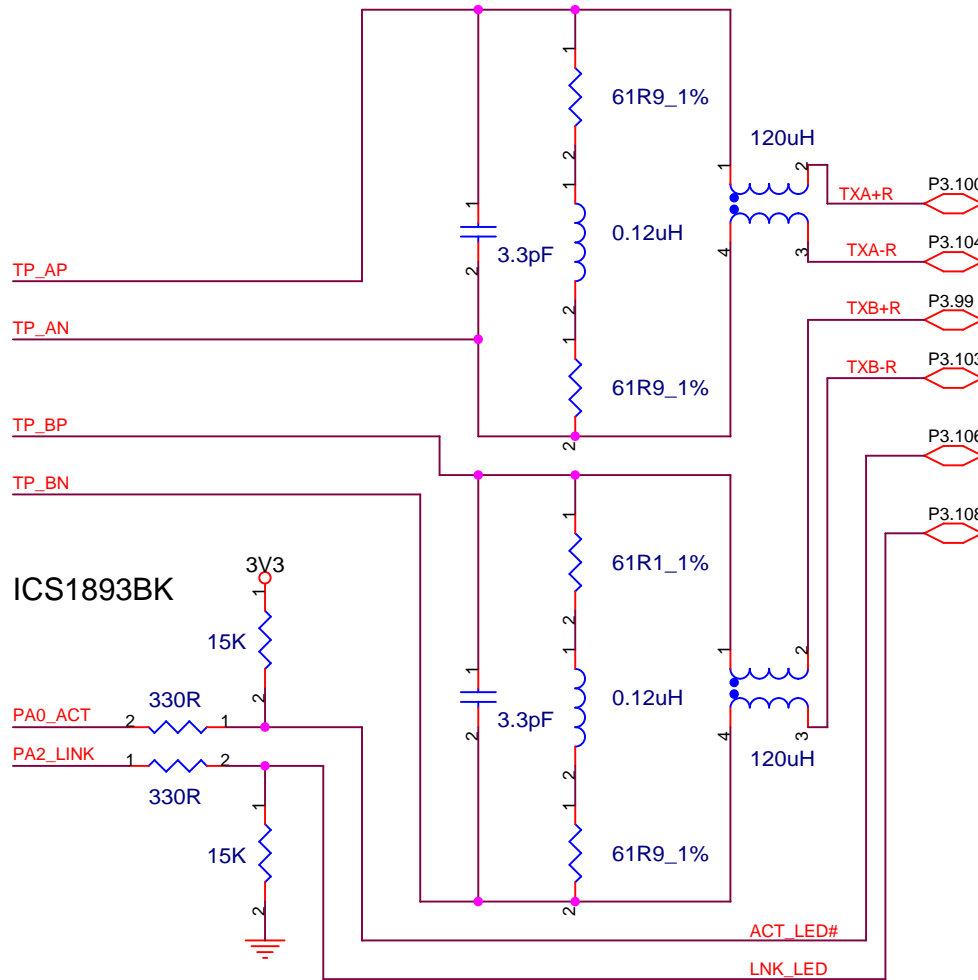


Baseboard



CC9C / Wi-9C External USB Host

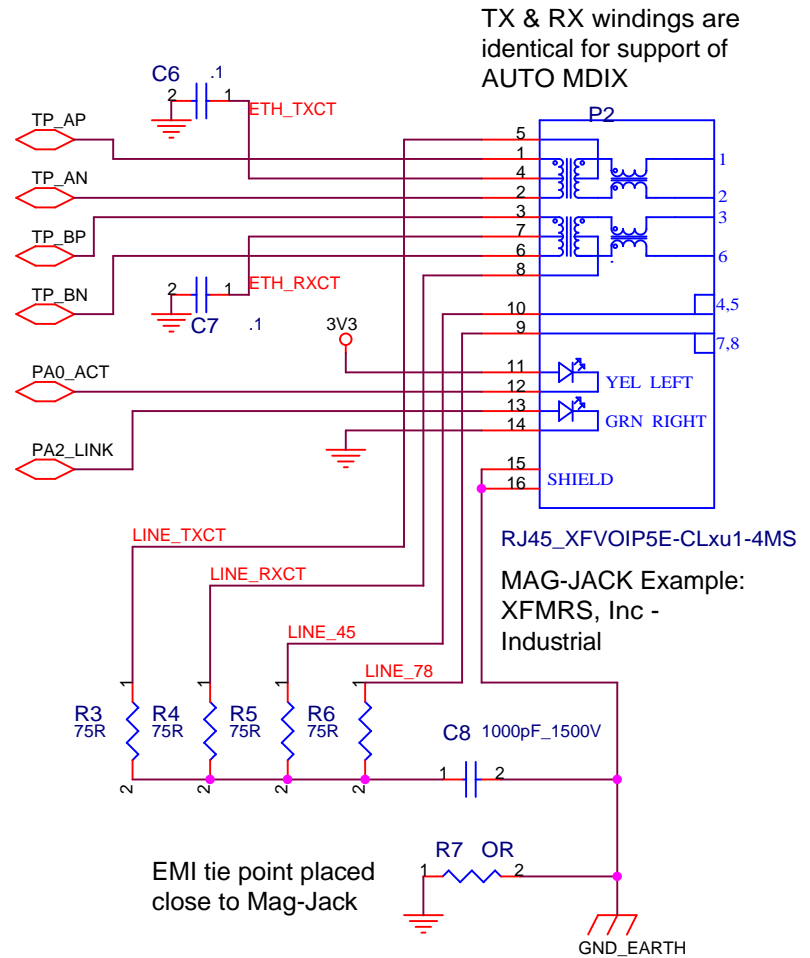
CC9C / Wi-9C Module



LED pins also function as PHY address bootstraps during power-up.

CC9C / Wi-9C External Ethernet

Baseboard



TX & RX windings are identical for support of AUTO MDIX

RJ45_XFVOIP5E-CLxu1-4MS
MAG-JACK Example:
XFMRS, Inc - Industrial

EMI tie point placed close to Mag-Jack

REV B changes 12pF to 3.3pF