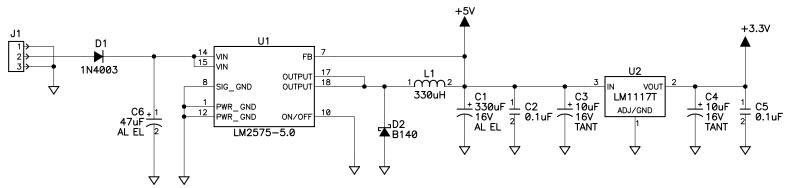
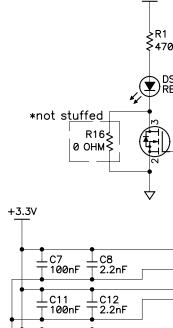
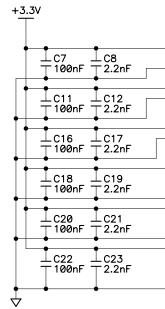
			REVISION HI	STORY			REVISION APPROVAL				
REV	EV ECO DESCI			CRIPTION			PROJECT ENGINEER	APPROVAL DATE	DOCUMENT CONTROL	APPROVAL DATE	
В	E14964	Converte	d thru—hole				нн	1/2/07	KF	1/2/07	
c	E15134	Initial Re	lease								
D	E15157		bbit RIO Proto								
		Updated d	<u>nout labeling e</u> lefault jumper	<u>error.</u> settings ta	ibles and a	offpage					
E F	E15222	connector		-							
	+	3.3V C7 100nF C11 100nF C16 100nF C18 100nF C20 100nF C22 100nF	Ffed R16 OHM C8 2.2nF C12 2.2nF C12 2.2nF C17 C17 C17 C19 2.2nF C19 2.2nF C21 C23	Power Indicat ED Q7 2N7002 1 1 1 64 16 17 33 32 48 49 8 24 41 56	U3:B VDDIO VSSIO VDDINT VSSIO VDDINT VDDIO	2000 - 20	↓ C9 ↓ 2.2nF ↓ C13 ↓ 2.2nF	+3.3V 			
						CO	PYRIGHT 2	009, DIGI	INTERNATI	ONAL	
		AWING CON	TENT:	TITLE							
REVISE			12/1/06			BIT Toty				BBIT NDUCTOR	
	JEFF NAUER 1/30/09 APPROVALS: INITIAL RELEASE		PROTOTYPIN			0					
PROJECT ENGINEER:			BOARD				www.ra	bbit.com			
HOEUN HAM											
_				B	09	90-0	0231				







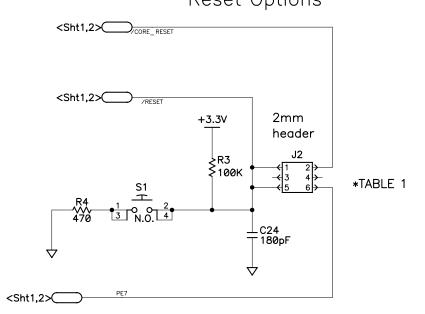
			REVISION HISTORY			REVISION APPROVAL					
	REV	ECO		DESC	RIPTION			PROJECT ENGINEER	APPROVAL DATE	DOCUMENT CONTROL	APPROVAL DATE
V	В	E14964	4 Converted thru-hole		нн	1/2/07	KF	1/2/07			
	С		Initial Release								
	D	E15157 c	Revise Ral correct pi	bbit RIO Proto <u>nout labeling e</u>	Board to rror.						
5	E	(Updated d	lefault iumper	settings to	ables and a	offpage				
1uF	F			IO CHIP PINOUT	IS AND NE	T NAMES					
		+3.		R16 OHM OHM C8 2.2nF C12 2.2nF C12 2.2nF C17 2.2nF C17 C17 C17 C2.2nF C19 C2.2nF C21 C2.2nF	Power Indica 20 07 2N7002 1 1 64 16 17 33 32		∠CORE_RESET_OU	 	+3.3V -C10 100nF -C14 100nF	_> <sht1,2></sht1,2>	
APPEND THE FOLLOWING DOCUMENTS WHEN CHANGING	DOUND		WING CON	TENT:	TITLE						
THIS DOCUMENT:		JN HAM	LEASE)	12/1/06		RAE	BIT	RIO		RAF	BIT
	REVISED	BY: F NAUER		1/30/09		PRO	TOT	(PIN)	G		DDLL NDUCTOR
		APPROVAL	LS: INITIAL	L RELEASE			OARI	D		www.ra	bbit.com
	ENGINEE	JN HAM			SIZE B		90-0	0231			
	XUA	N TRUON		DATE	SCALE	NONE	RELEASE DATE			^{SHEET} 1	^{OF} 4

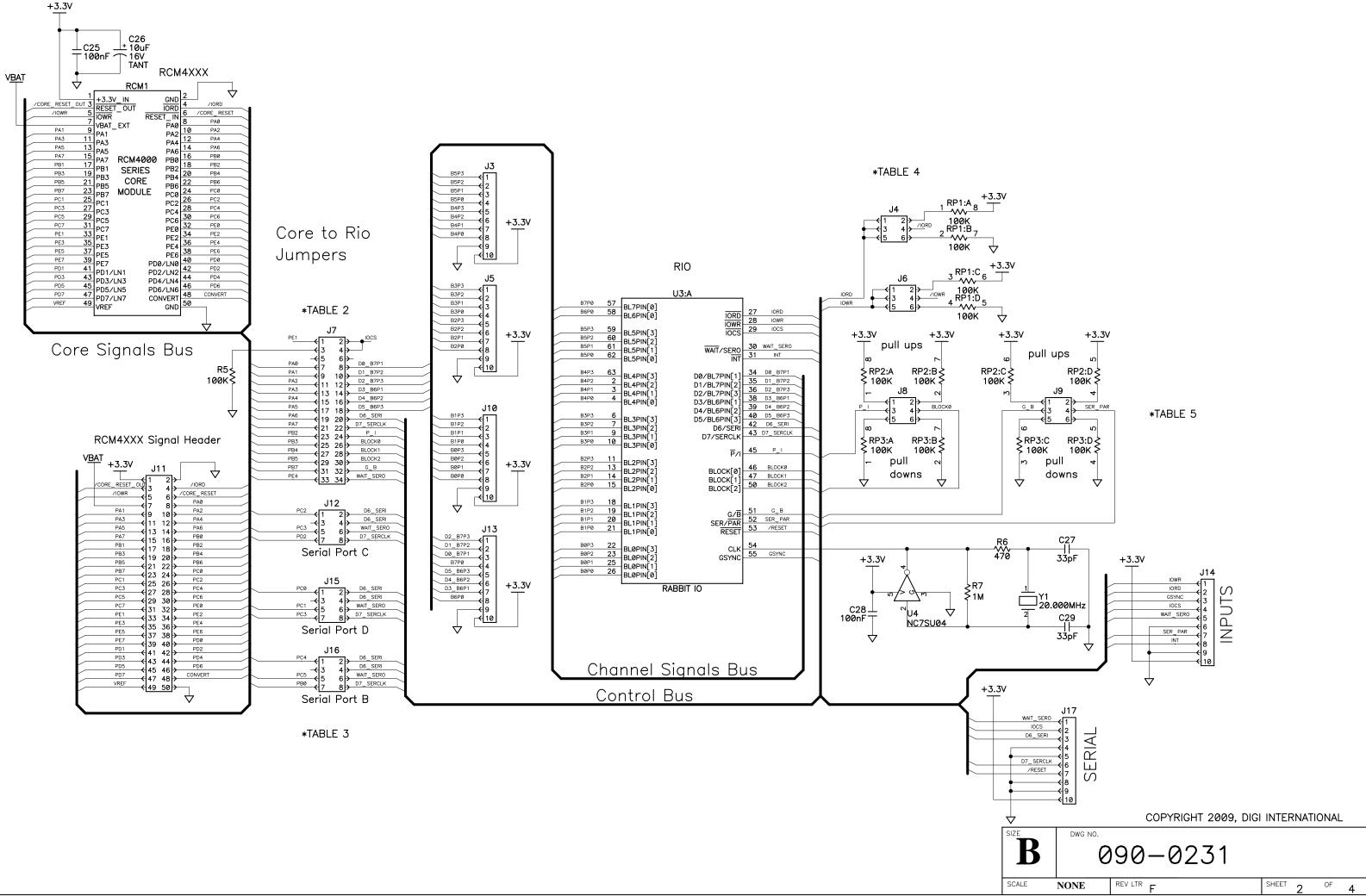


⊥C15 ⊤100nF

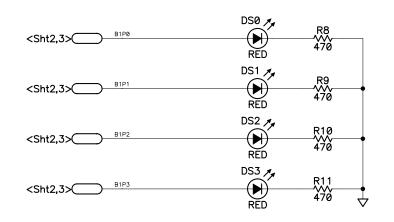
 $-\frac{1}{2}$ + $\frac{1}{1}$ $\frac{R2}{2}$ + $\frac{1}{1.3K}$

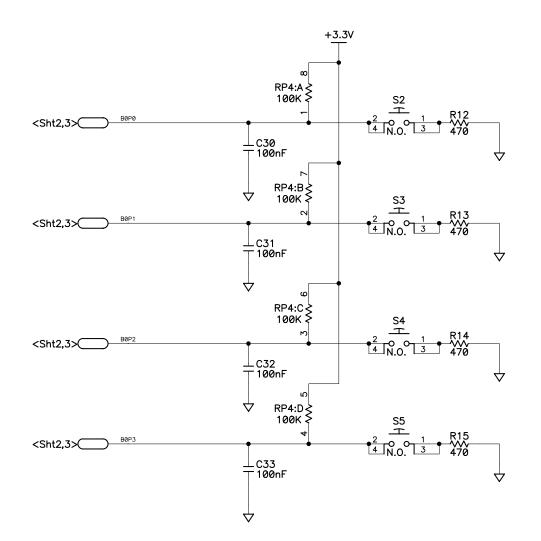
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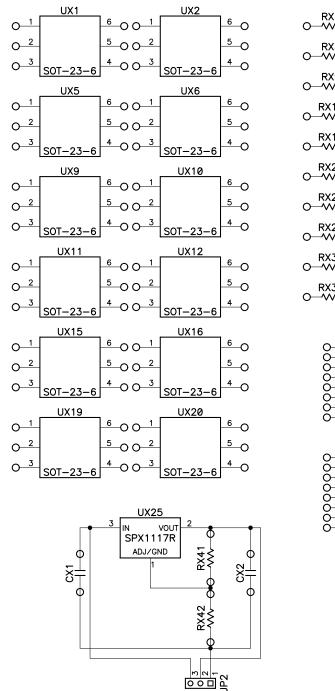




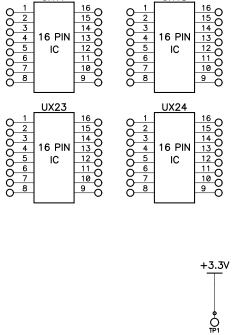


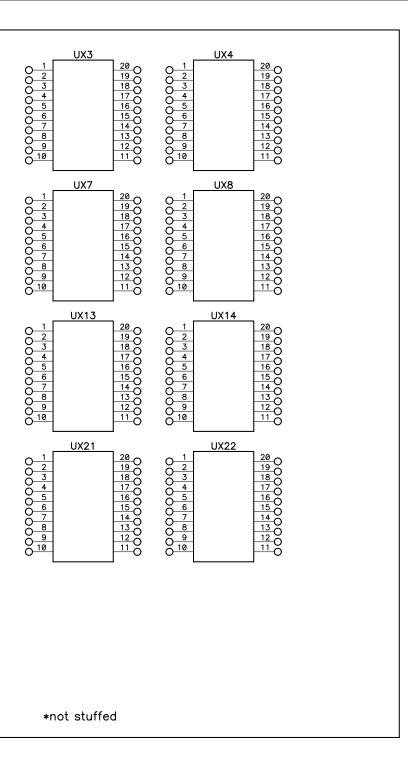






RX1 0 RX2 RX3 RX4 0 RX4 0 RX4 0_____0 RX6 RX7 0_____0 RX10 0—_____0 0-0____0___0 RX18 0—~~~0 RX19 0—VVV—O RX21 RX22 RX23 RX24 RX25 RX26 RX27 RX28 RX29 RX30 RX31 RX32 RX33 0—~~~0 RX34 RX35 RX36 RX37 RX38 RX39 UX17 UX18







*TABLE 1: RESET JUMPERS

*TABLE 2:	CORE MODULE	
	PARALLEL MODE JU	MPERS

*TABLE 3: CORE MODULE

J2	FUNCTION	
1–2	CORE RESET SAME AS RIO RESET	
3–4	NOT JUMPERED	
5–6	RIO RESET COMES FROM PE7 (don't use S1)	DEFAULT

	•	
J7	PIN FUNCTION	
1-2	RIO /CS on PE1	DEFAULT
3-4	3-4 RIO /CS PULLED LOW	
5-6	nc	
7–8	RIO DØ on PAØ	
9-10	RIO D1 on PA1	
11-12	RIO D2 on PA2	
13-14	RIO D3 on PA3	
15-16	RIO D4 on PA4	
17-18	RIO D5 on PA5	
19-20	RIO D6 on PA6	
21-22	RIO D7 on PA7	
23-24	RIO /P/I on PB2	
25-26	RIO BLOCKØ on PB3	
27-28	RIO BLOCK1 on PB4	
29-30	RIO BLOCK2 on PB5	
31-32	RIO G//B on PB7	
33–34	RIO /WAIT on PE4	

с	JUMPER	PIN FUNCTION	
PORT	J12:1-2	RIO SERI on PC2	
PC	J12:5-6	RIO SERO on PC3	
CORE SERIAL	J12:4-6	BIDIRECTIONAL DATA on PC2	
SEL	J12:7-8	RIO SERCLK on PD2	
Δ			·
PORT	J15:1-2	RIO SERI on PC0	DEFAULT
P C	J15:5-6	RIO SERO on PC1	
CORE SERIAL	J15:4-6	BIDIRECTIONAL DATA on PC0	·
SEF	J15:7-8	RIO SERCLK on PC3	DEFAULT
ш			
PORT	J16:1-2	RIO SERI on PC4	
Å.	J16:5-6	RIO SERO on PC5	
CORE SERIAL	J16:4-6	BIDIRECTIONAL DATA on PC4	
SEF	J16:7-8	RIO SERCLK on PB0	

*TABLE 4 READ/WRITE STROBE JUMPERS

J4	FUNCTION					
1-2	RIO /RD PULLED HIGH					
3-4	RIO /RD IS CORE /IORD					
5-6	RIO /RD PULLED LOW					
J6						
1-2	RIO /WR PULLED HIGH					
3-4	RIO /WR IS CORE /IOWR					
5-6	RIO /WR PULLED LOW					

*TABLE 5 MODE SELECT JUMPERS

	JUMPER S	ETTINGS				LOGIC VA	ALUE	
P I	BLOCKØ	G B	SER PAR	MODE	Р I	BLOCKØ	ଜା ପ	SER PAR
Х	Х	Х	J9:4-6	Parallel	Х	Х	Х	0
J8:3-5	х	J9:3-5	J9:2-4	RabbitNet Device	0	X	0	1
J8:1-3	х	J9:3-5	J9:2-4	RabbitNet Hub	1	х	0	1
J8:3-5	J8:4–6	J9:1-3	J9:2-4	SPI – LSB first	0	0	1	1
J8:1-3	J8:4–6	J9:1-3	J9:2-4	Bidir Data — LSB first	1	0	1	1
J8:3-5	J8:2-4	J9:1-3	J9:2-4	SPI – MSB first	0	1	1	1
J8:1-3	J8:2-4	J9:1-3	J9:2-4	Bidir Data — MSB first	1	1	1	1

SERIAL MODE JUMPERS

DEFAULT

