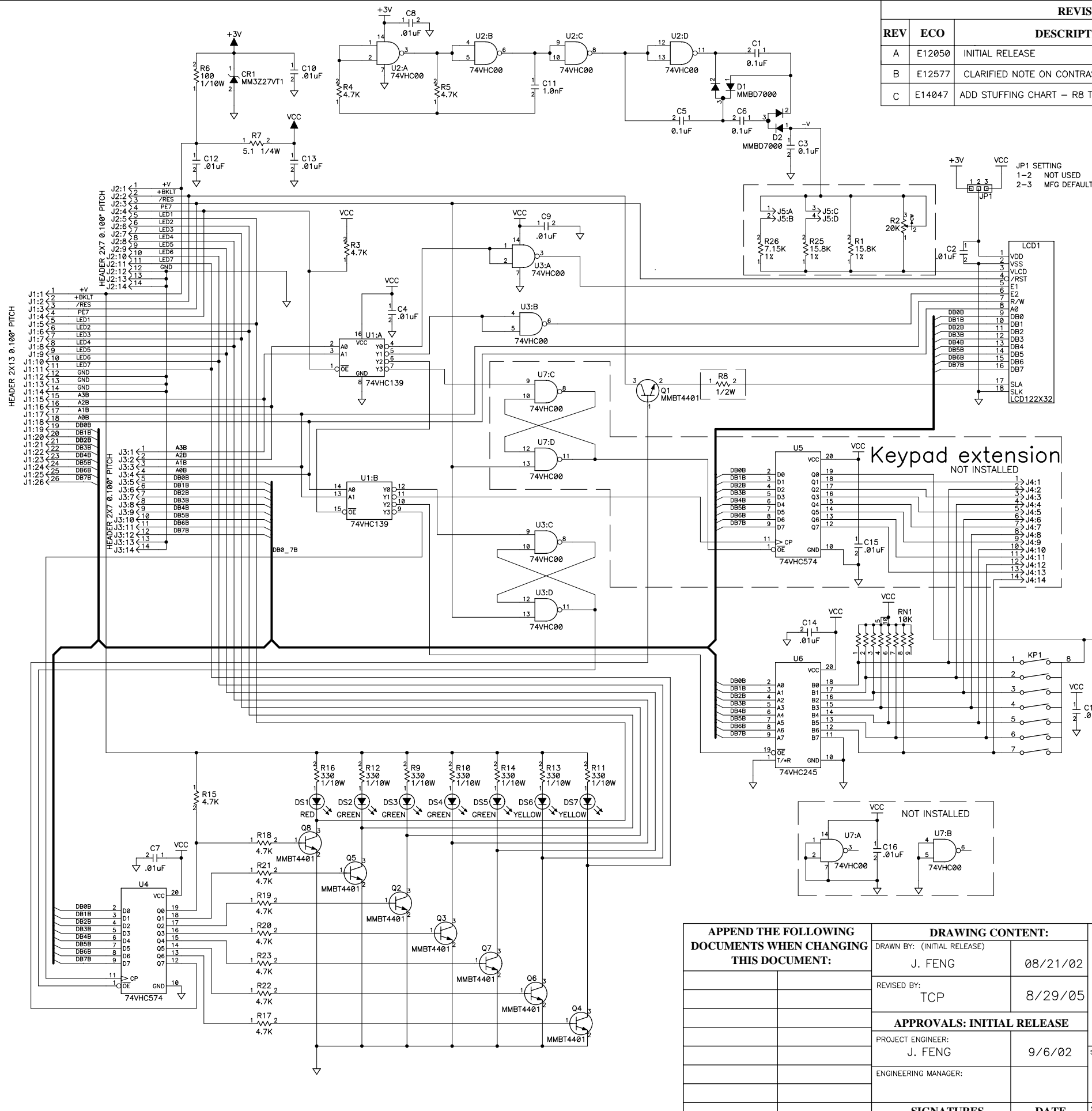


REVISION HISTORY			REVISION APPROVAL			
REV	ECO	DESCRIPTION OF CHANGE	PROJECT ENGINEER	APPROVAL DATE	DOCUMENT CONTROL	APPROVAL DATE
A	E12050	INITIAL RELEASE	JF	9/6/02	KIS	9/6/02
B	E12577	CLARIFIED NOTE ON CONTRAST CONTROL COMPONENTS	EP	2/23/04	KF	2/23/04
C	E14047	ADD STUFFING CHART – R8 TO SUPPORT BOTH +3.3V AND +5.0V SYSTEM	TCP	9/28/05	KF	9/28/05



STUFFING TABLE

		+3.3V OPTION	+5V OPTION
BACKLIGHT CONTROL	R8	3.3 OHM	15 OHM
CONTRAST CONTROL	R2	20K POT	20K POT
	J5:	1-2 (2.8V)	NOT INSTALLED
		3-4 (3.3V)	NOT INSTALLED
	N/A (5V)	NOT INSTALLED	NOT INSTALLED
	R25,R26,R1	NOT INSTALLED	NOT INSTALLED

POWER TABLE

REF DES	DEVICE	DEVICE VOLTAGE INFORMATION				FILTER CAP
		GND	VCC	+3V	NC	
U1	74VHC139	8	16			C4
U2	74VHC00	7		14		C8
U3	74VHC00	7	14			C9
U4	74VCT574	10	20			C7
U5	74VCT574	10	20			C15
U6	74VHC245	10	20			C14
U7	74VHC00	7	14			C16

- NOTES: UNLESS OTHERWISE SPECIFIED;
- ALL CAPACITORS ARE 50VDC OR HIGHER.
  - THE ORIGIN SOURCE OF A VOLTAGE IS REPRESENTED BY (VCC), AND ALL REFERENCES TO THAT VOLTAGE ARE REPRESENTED BY (VCC).
  - OUTLINED CIRCUIT MAY NOT BE STUFFED DEPENDING ON MODEL. SEE STUFFING CHART.

APPEND THE FOLLOWING DOCUMENTS WHEN CHANGING THIS DOCUMENT:	DRAWING CONTENT:		TITLE <b>SCHEMATIC DIAGRAM KEYPAD DISPLAY UNIT 2.7-5V Version</b>	 2900 SPAFFORD ST. DAVIS, CA 95616 530-757-4616
	DRAWN BY: (INITIAL RELEASE) <b>J. FENG</b>	DATE <b>08/21/02</b>		
	REVISED BY: <b>TCP</b>	DATE <b>8/29/05</b>	PROJECT ENGINEER: <b>J. FENG</b>	DATE <b>9/6/02</b>
	APPROVALS: INITIAL RELEASE		PROJECT ENGINEER: <b>J. FENG</b>	DATE <b>9/6/02</b>
	ENGINEERING MANAGER:		ENGINEERING MANAGER:	
	SIGNATURES	DATE	SCALE <b>NONE</b>	RELEASE DATE <b>08/21/02</b>
			SIZE <b>C</b>	DWG NO. <b>090-0156</b>
			SHEET <b>1</b> OF <b>1</b>	

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