Decoupling Capacitors

NOTES: UNLESS OTHERWISE SPECIFIED;
1. ALL RESISTOR VALUES ARE IN OHMS, 1/16W, 5%
2. ALL CAPACITORS ARE 50VDC OR HIGHER.
3. THE ORIGINATION SOURCE OF A VOLTAGE IS REPRESENTED BY (VCC), AND ALL REFERENCES TO THAT VOLTAGE ARE REPRESENTED BY (VCC).

4. OUTLINED CIRCUIT MAY NOT BE STUFFED DEPENDING ON MODEL, SEE STUFFING CHART FOR CLARIFICATION.

5. COMPONENT VALUES SHOWN WITH AN ASTERISK (*) FOLLOWING THE VALUE, MAY HAVE DIFFERENT VALUES, OR MAY NOT BE STUFFED DEPENDING ON MODEL. SEE STUFFING CHART FOR CLARIFICATION.

TABLE A

REF DES	DEVICE	DEVICE VOLTAGE INFORMATION					DEVICE: FILTER CAP REF DES(s)
		AGND	GND	VCC	VRAM	uPBAT	
U1	74VHC245		10	20			C5
U3	RTL8019AS		14,28,44 52,83,86	6,17,47 57,70,89			C7,10,12,13,15,16
U4	RABBIT 2000		2,27,39 52,77,89	3,28,53, 78,92		42	C14, 57, 52, 54, 58 C25 – PIN42 (uPBAT)
U5	74VHC244		10	20			C21
U6	74VHC244		10	20			C22

STUFFING TABLE

	CIRCUIT	PART	RCM2100	RCM2110	RCM2120	RCM2130
SRAM	FIRST	U12	512K	128K	512K	128K
	SRAM select	JP3	2–3	1–2	2–3	1–2
	Filter Capacitor	C56	INSTALLED	INSTALLED	INSTALLED	INSTALLED
	SECOND	U9	NOT INSTALLED	NOT INSTALLED	NOT INSTALLED	NOT INSTALLED
	Filter Capacitor	C59, C60	NOT INSTALLED	NOT INSTALLED	NOT INSTALLED	NOT INSTALLED
	BUFFER	U8	INSTALLED	NOT INSTALLED	INSTALLED	NOT INSTALLED
	Bypass Jumper	RP1, RP2	NOT INSTALLED	INSTALLED	NOT INSTALLED	INSTALLED
	Filter Capacitor	C61	INSTALLED	NOT INSTALLED	INSTALLED	NOT INSTALLED
FLASH	FIRST	U10	256K	256K	256K	256K
	SRAM select	JP1	1–2	1–2	1–2	1–2
	Filter Capacitor	C50	INSTALLED	INSTALLED	INSTALLED	INSTALLED
	SECOND	U11	256K	NOT INSTALLED	256K	NOT INSTALLED
	SRAM select	JP2	1–2	NOT INSTALLED	1–2	NOT INSTALLED
	Filter Capacitor	C55	INSTALLED	NOT INSTALLED	INSTALLED	NOT INSTALLED
ETHERNET OPTIONS	U3		INSTALLED	INSTALLED	NOT INSTALLED	NOT INSTALLED
	J3, C1, C2		NOT INSTALLED	NOT INSTALLED	NOT INSTALLED	NOT INSTALLED
	J4		INSTALLED	INSTALLED	NOT INSTALLED	NOT INSTALLED
	R1, R2 DS1, DS2		INSTALLED	INSTALLED	NOT INSTALLED	NOT INSTALLED
	R6, C3, C4		INSTALLED	INSTALLED	NOT INSTALLED	NOT INSTALLED
	R10, R11, R19, C3, C4, C7, C8, C9, C11 C12, C13, C15, C16		INSTALLED	INSTALLED	NOT INSTALLED	NOT INSTALLED
	Y1		INSTALLED	INSTALLED	NOT INSTALLED	NOT INSTALLED
PORTS D & E OPTIONS		R21, R24, R35 R36, R37, R38	NOT INSTALLED	NOT INSTALLED	INSTALLED	INSTALLED
INTERRUPT/JUMPER OPTIONS		R12, R18, R25 R58, R59	NOT INSTALLED	NOT INSTALLED	NOT INSTALLED	NOT INSTALLED

Title Block

APPEND THE FOLLOWING DOCUMENTS WHEN CHANGING THIS DOCUMENT:

DRAWING CONTENT:
DRAWN BY: (INITIAL RELEASE) KAH — 25JUL00
REVISED BY: NDH — 11/2/09

APPROVALS: INITIAL RELEASE
PROJECT ENGINEER: D.MUSGROVE — 2/15/01
ENGINEERING MANAGER: — — — —

SIGNATURES / DATE

TITLE: SCHEMATIC DIAGRAM RCM2100 SERIES RABBITCORE

RABBIT SEMICONDUCTOR
www.rabbit.com

SIZE: C
DWG NO. 090–0114
SCALE: NONE
RELEASE DATE: 11/2/09
SHEET 1 OF 2

PROCESSOR

SRAM*

FLASH 1*

FLASH 2*

RABBIT 2000

SRAM SELECT*
128K SRAM 1–2
512K SRAM 2–3

FLASH 2 SELECT*
1–2 128K/256K FLASH
2–3 512K FLASH

FLASH 1 SELECT*
1–2 128K/256K FLASH
2–3 512K FLASH

BANK SELECT
1–2 NORMAL MODE
2–3 BANK MODE

POWER TO VRAM SWITCH

CS Control

RESET GENERATOR

RABBIT PROGRAMMING IN PORT

ETHERNET

BATTERY BACK-UP

11/2/09

SIZE: C

DWG NO. 090–0114

SCALE: NONE   REV LTR: G   SHEET 2 OF 2